

# Scalable Asynchronous Contact Mechanics using Charm++

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- Problems with other existing algorithms
  - An object can end up going through itself or another object
  - Violate physical properties

What you want

What you get

What you want



What you get



What you want



What you get



What you want



What you get



**incorrect handling of collisions**

# Parallelization Challenges

- **Highly irregular communication pattern**

- *Message driven execution in Charm++*

- **Dynamic load imbalancing**

- *Adaptive runtime system*

- **Very fine grained computation**

- *Overlapping computation and communication*

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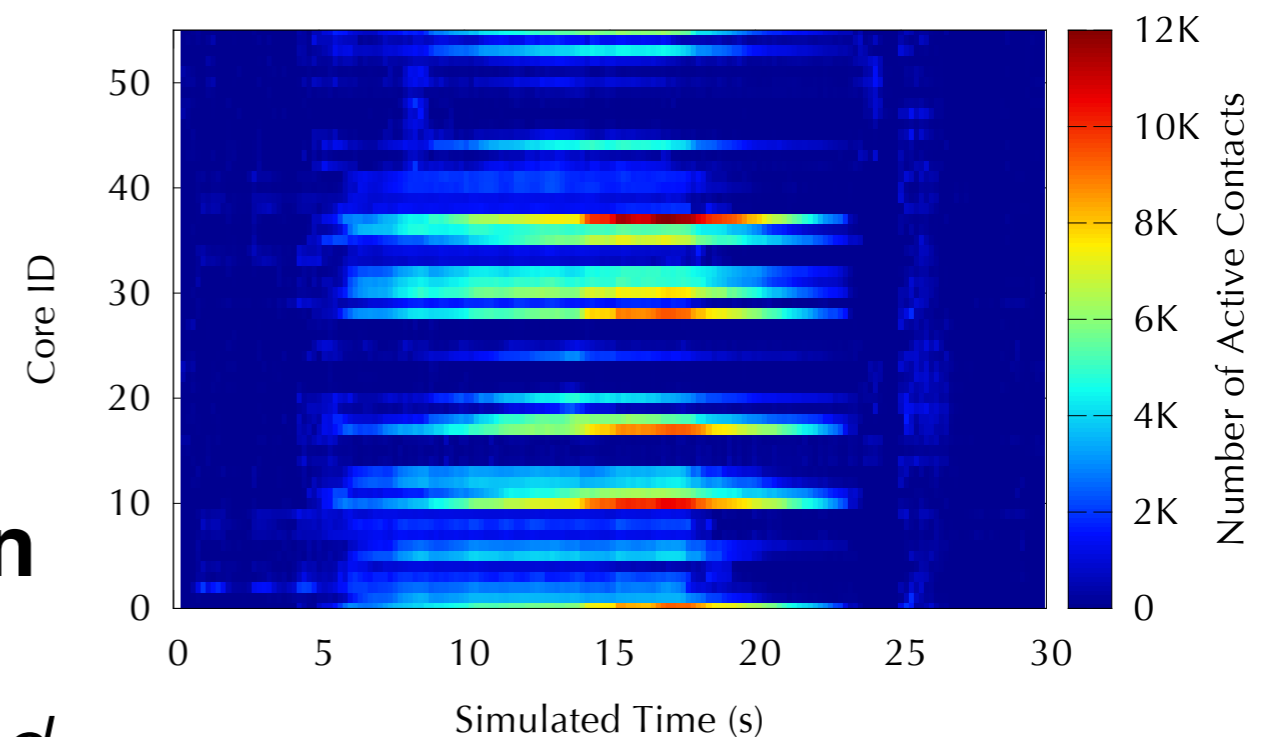
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# Overall Flow

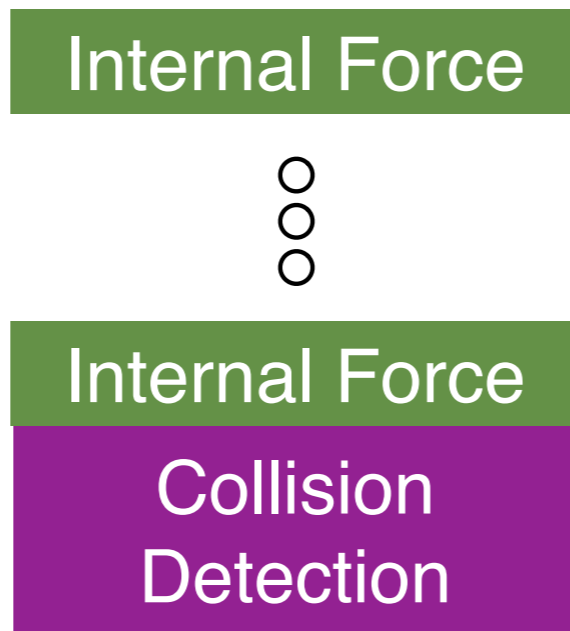
# Overall Flow

Internal Force

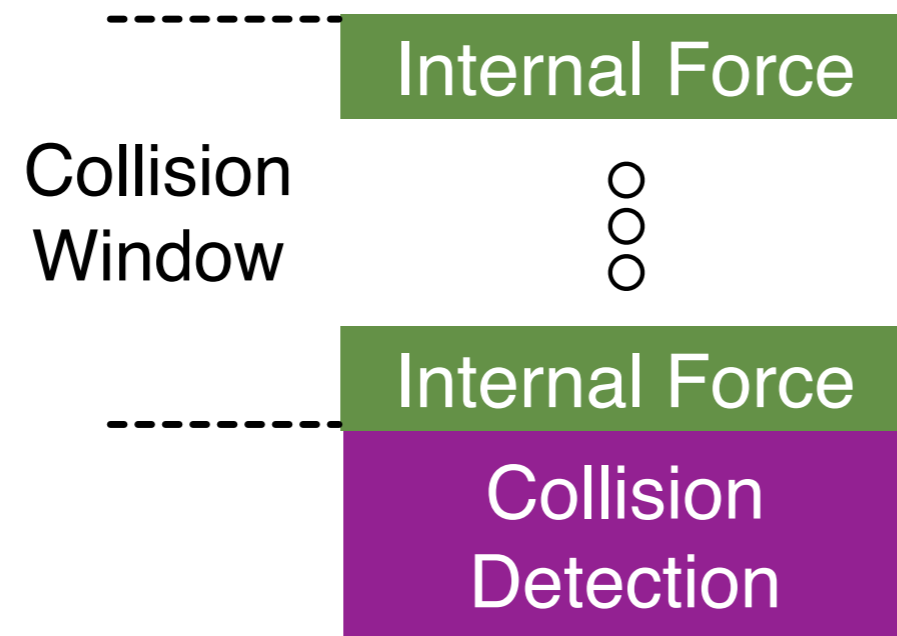


Internal Force

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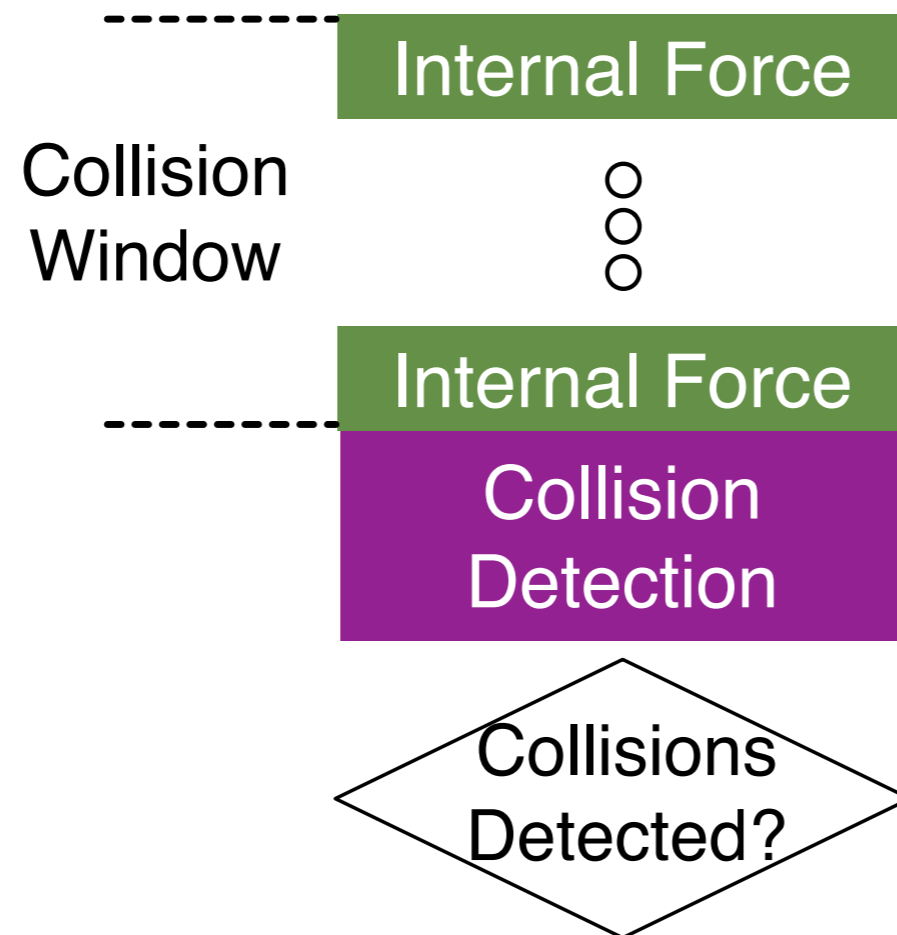


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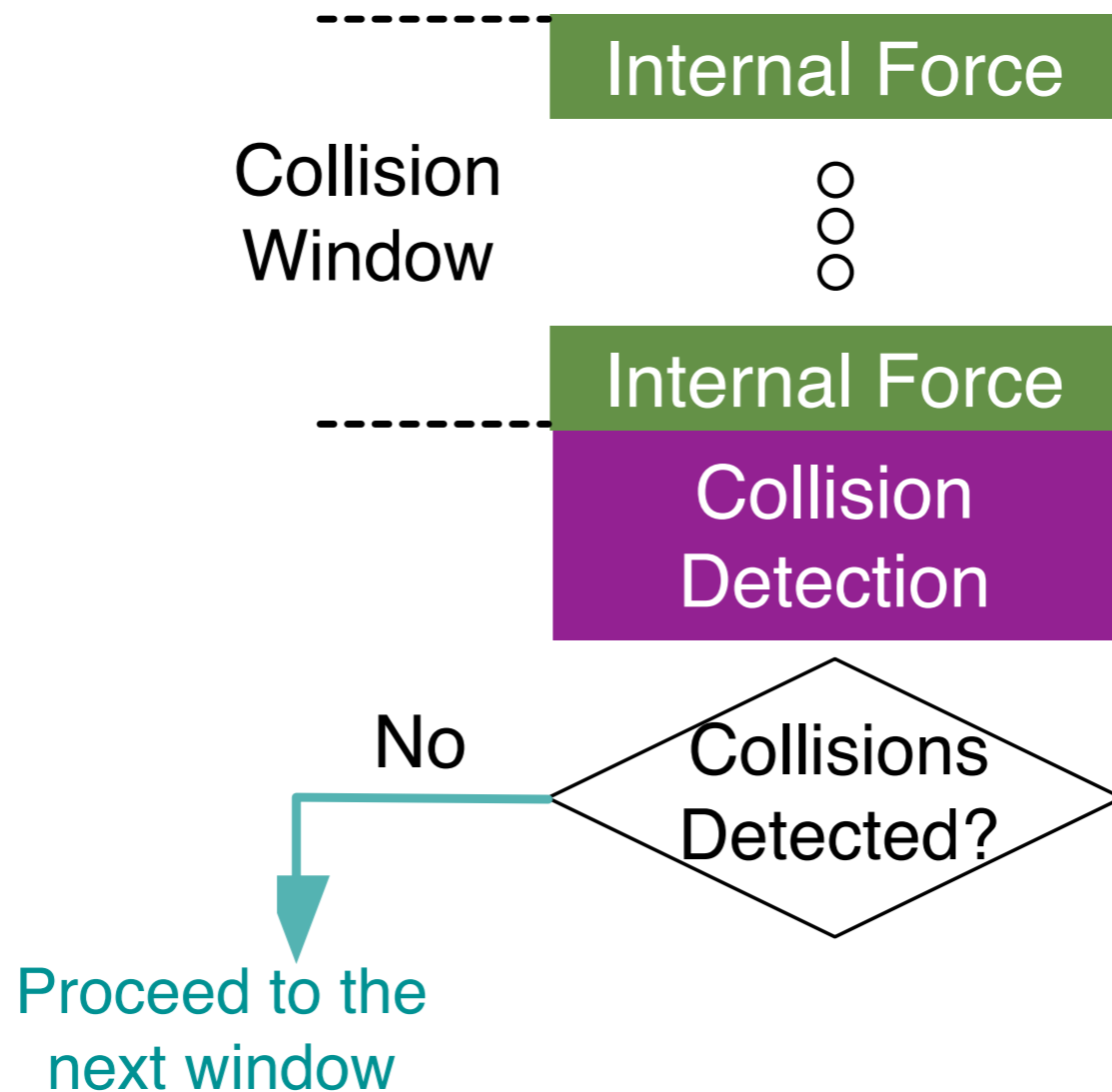




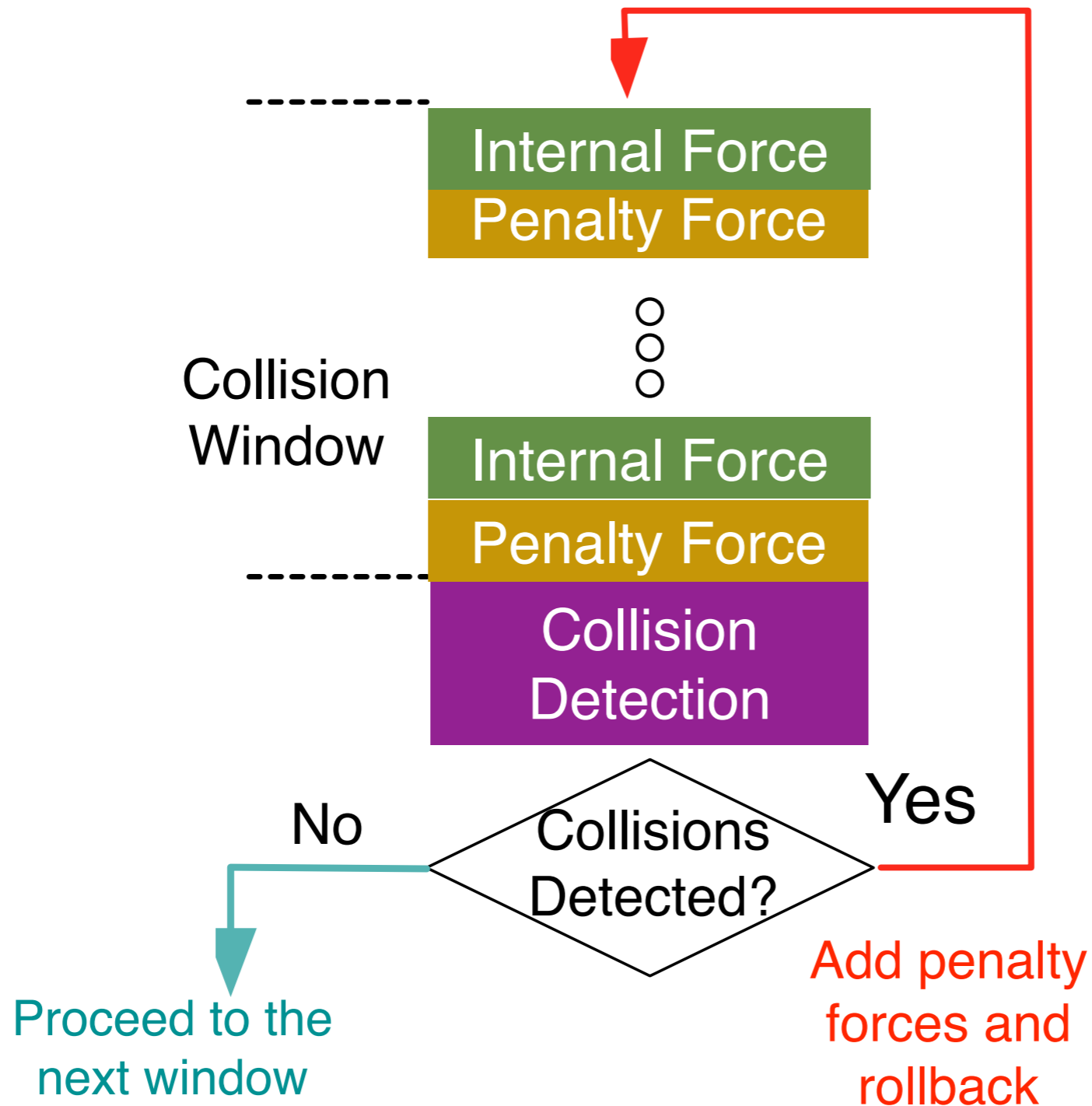
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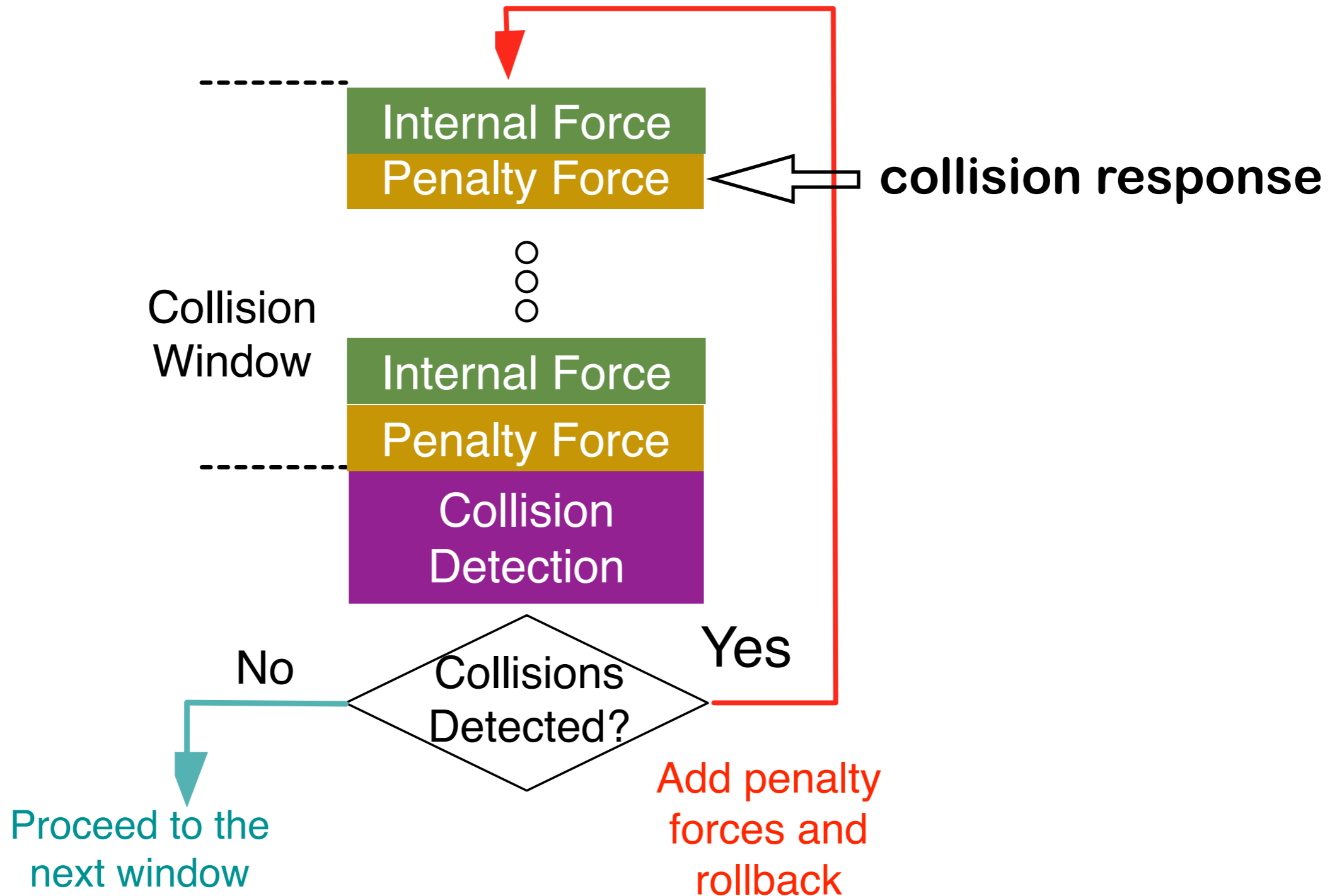
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Broad Phase

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Locally inside each partition, we use a **26-DOP hierarchy** to fit the swept volumes of the triangle to detect **potential** collisions.

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Globally among all the partitions, we fit the trajectory of each triangle to a 3D bounding box and then pass them to the **existing collision detection library** in Charm++.

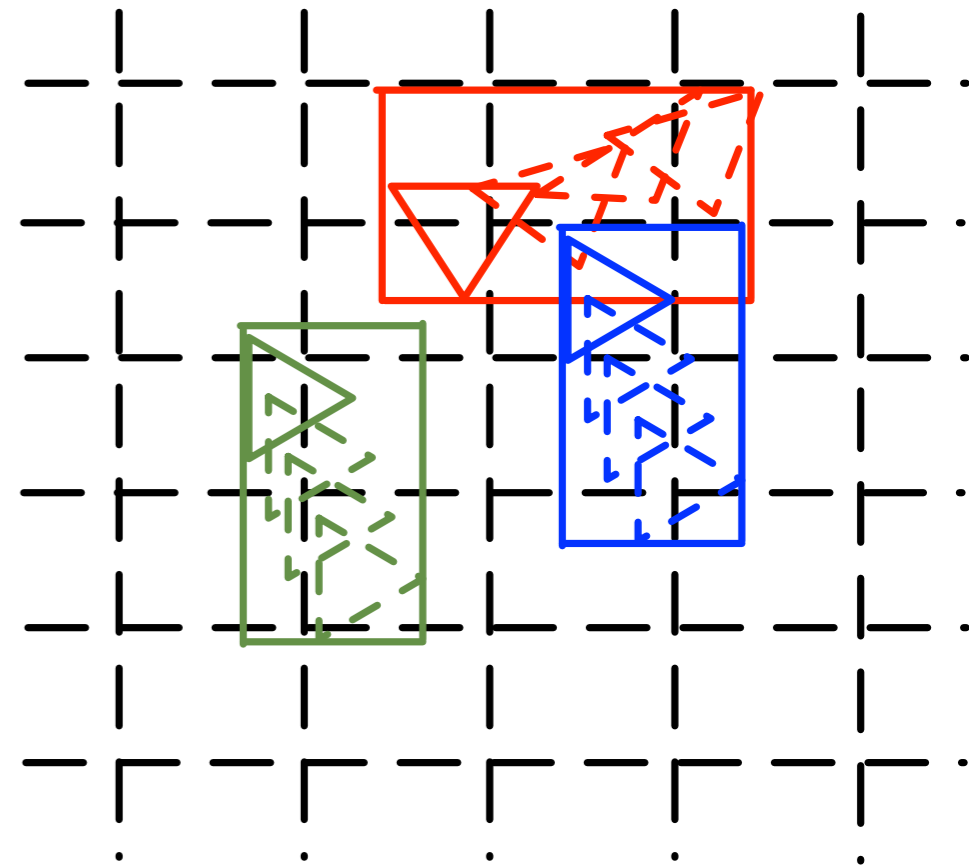


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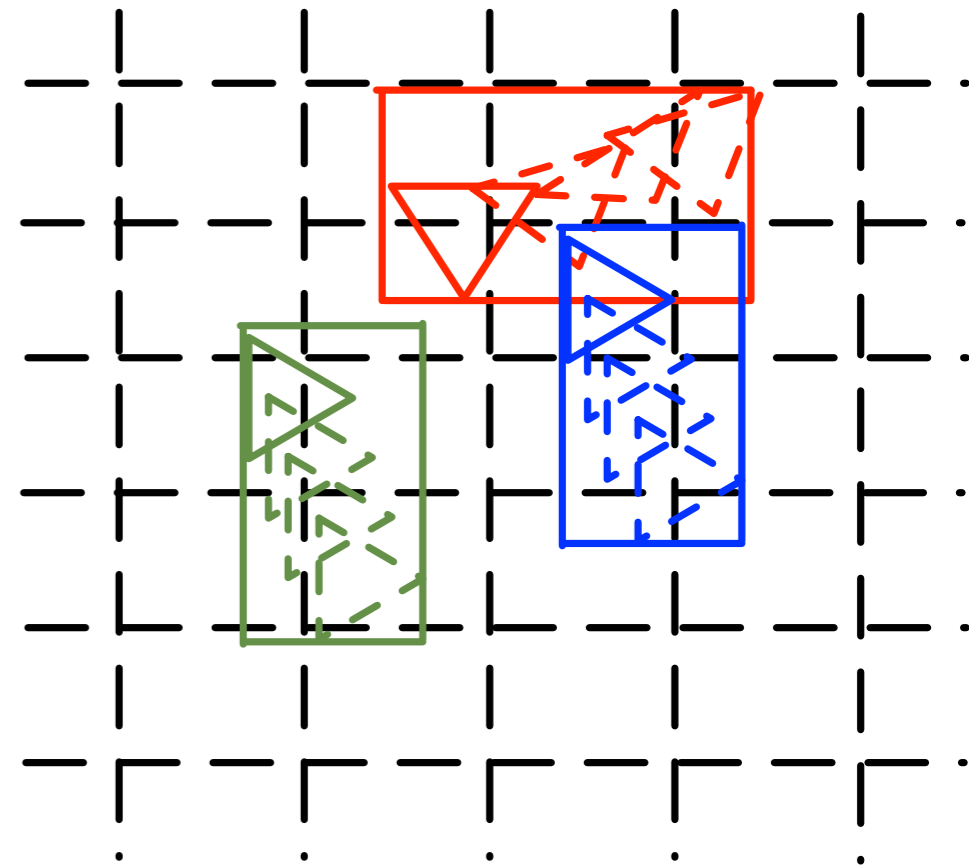
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# Collision Detection

## Narrow Phase

We apply the space-time separating planes method to filter out potential collisions.

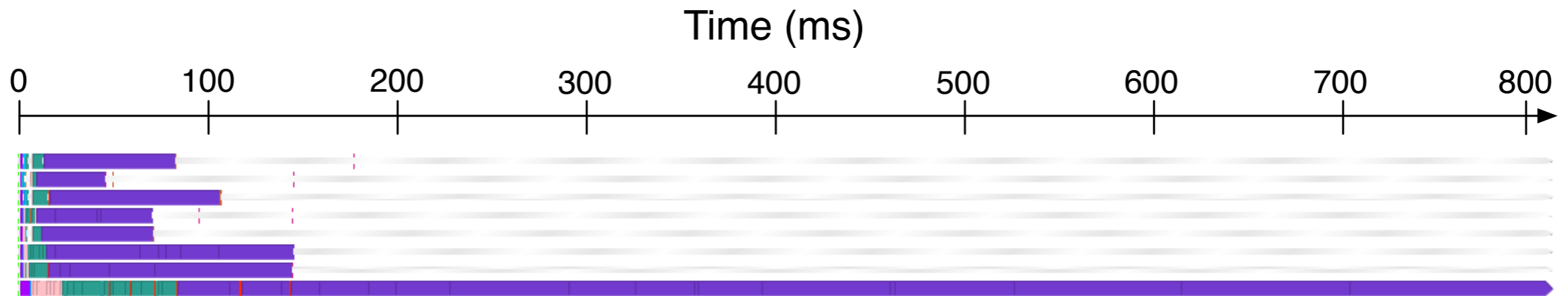


# Narrow Phase

First Challenge: **Computation Imbalance**

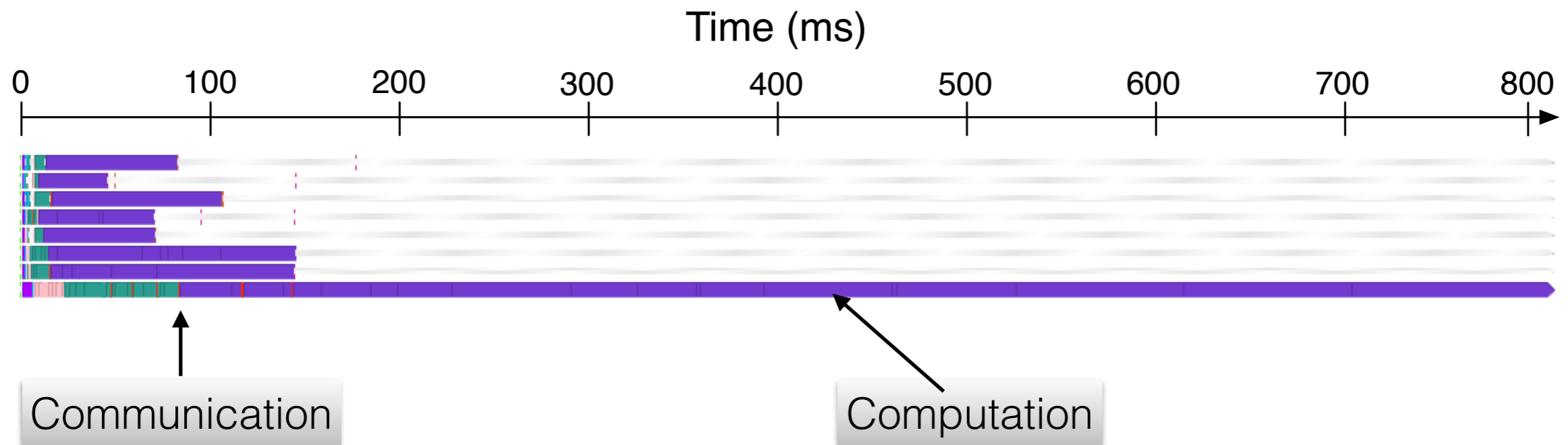
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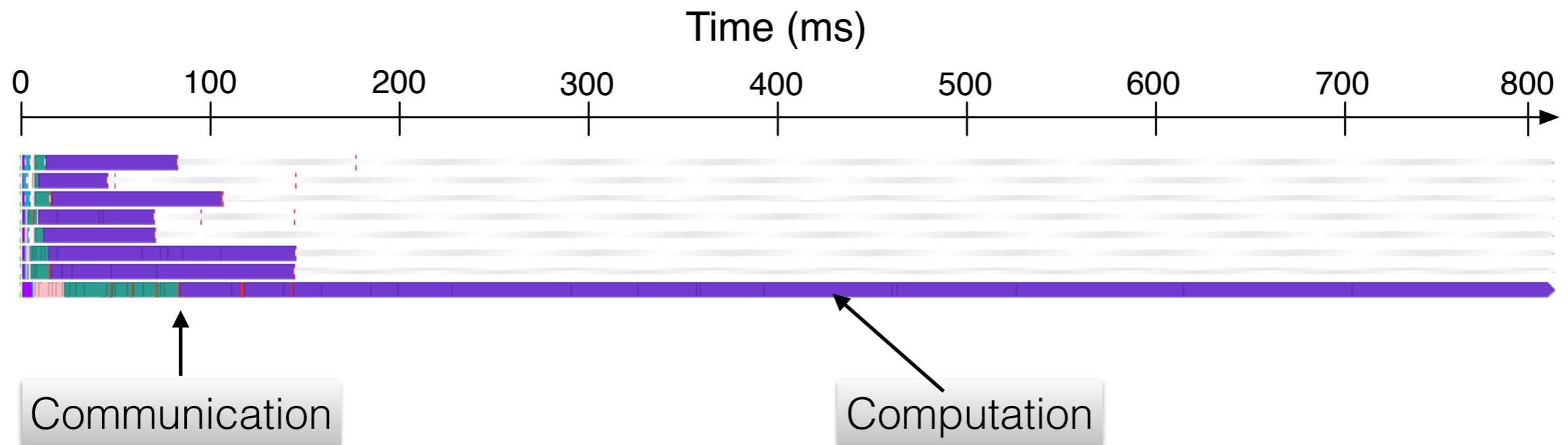
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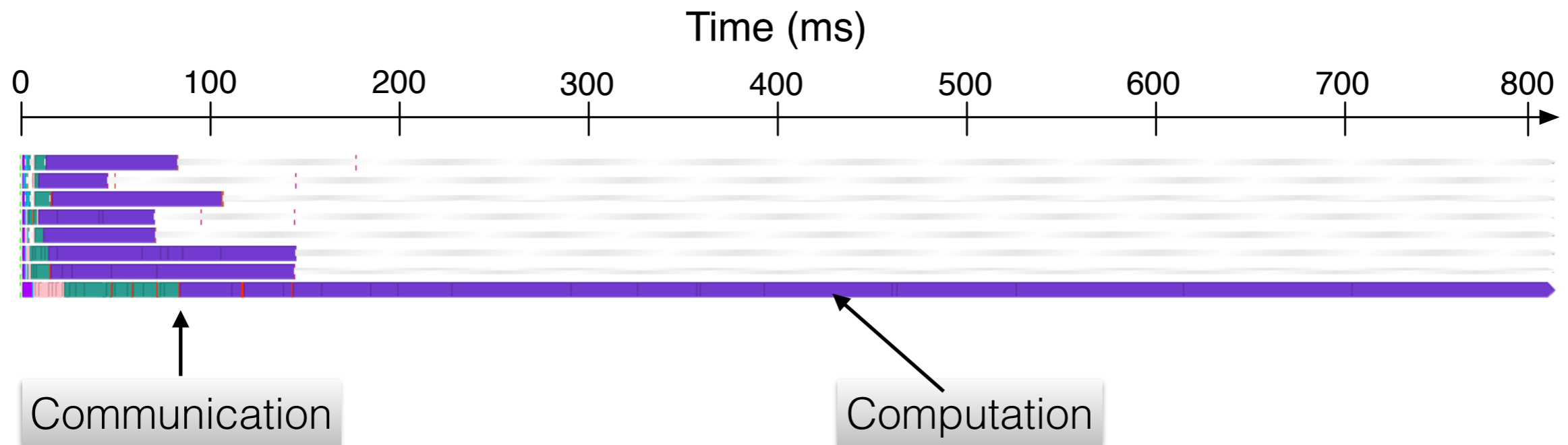
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Time spent on each potential collision pair is **not uniform**

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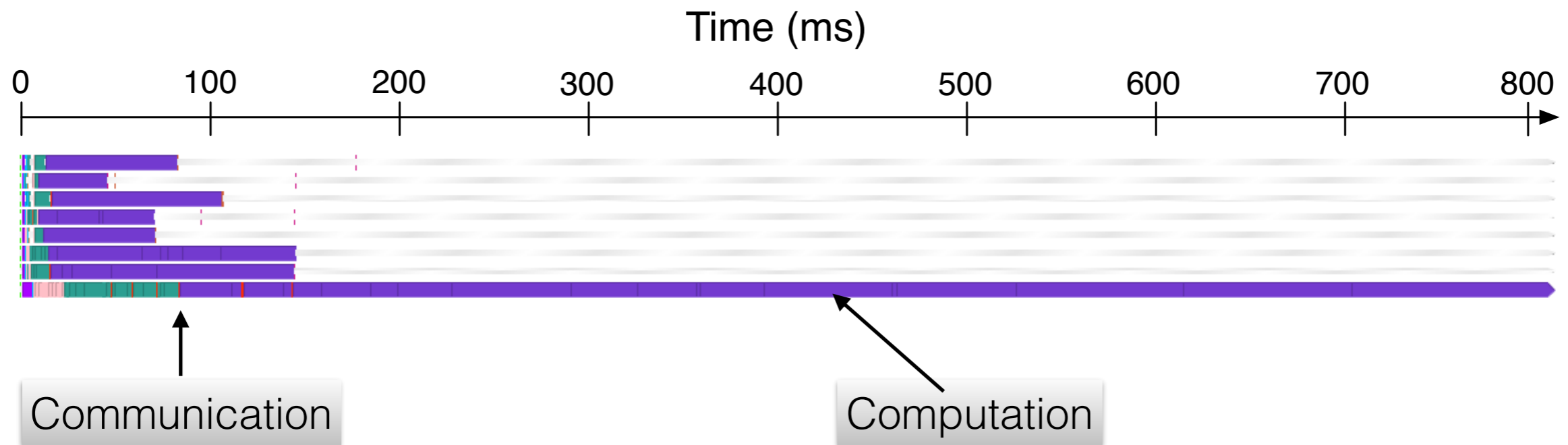


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Detection time depends on **trajectory length** of each vertex in the potential pair

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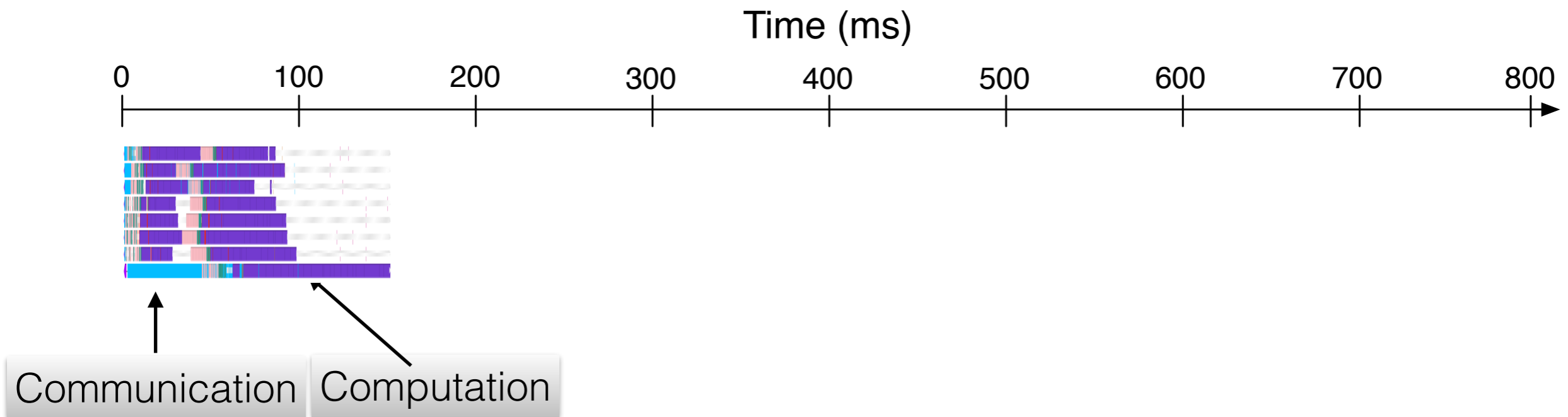
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A **profiling** based load balancer



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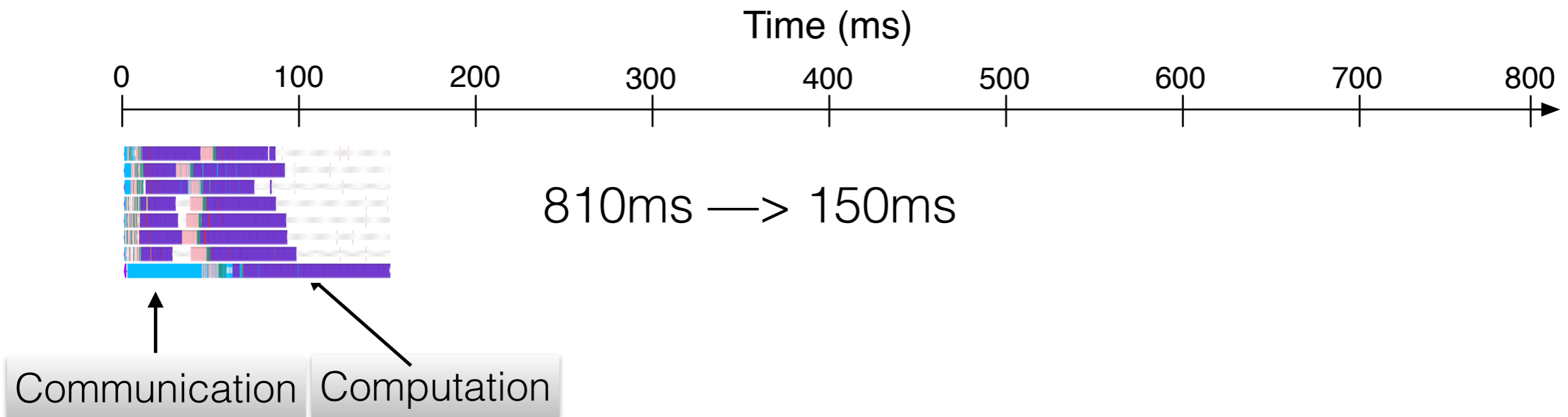
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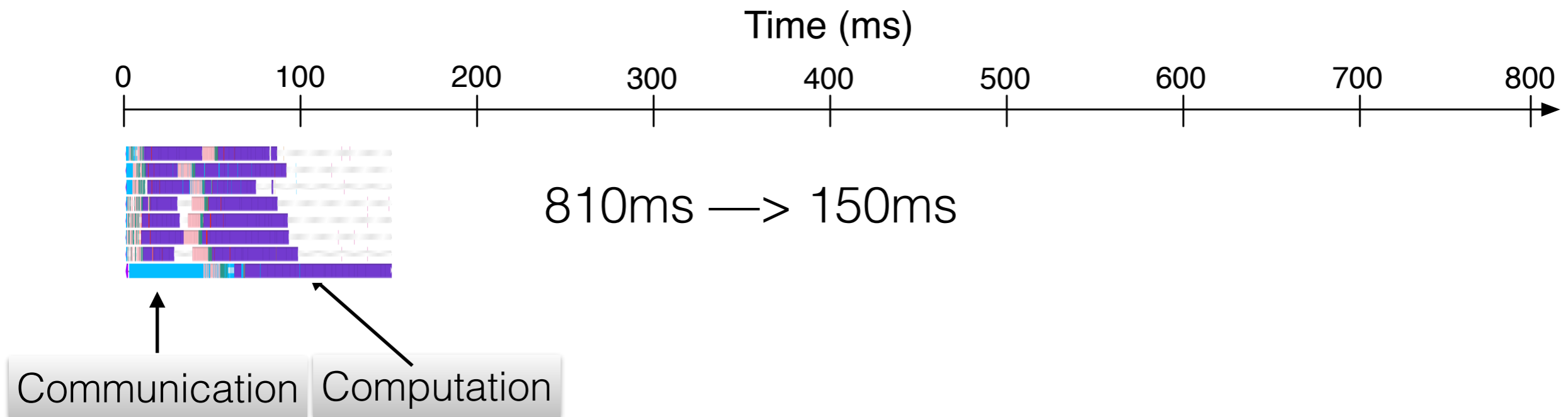
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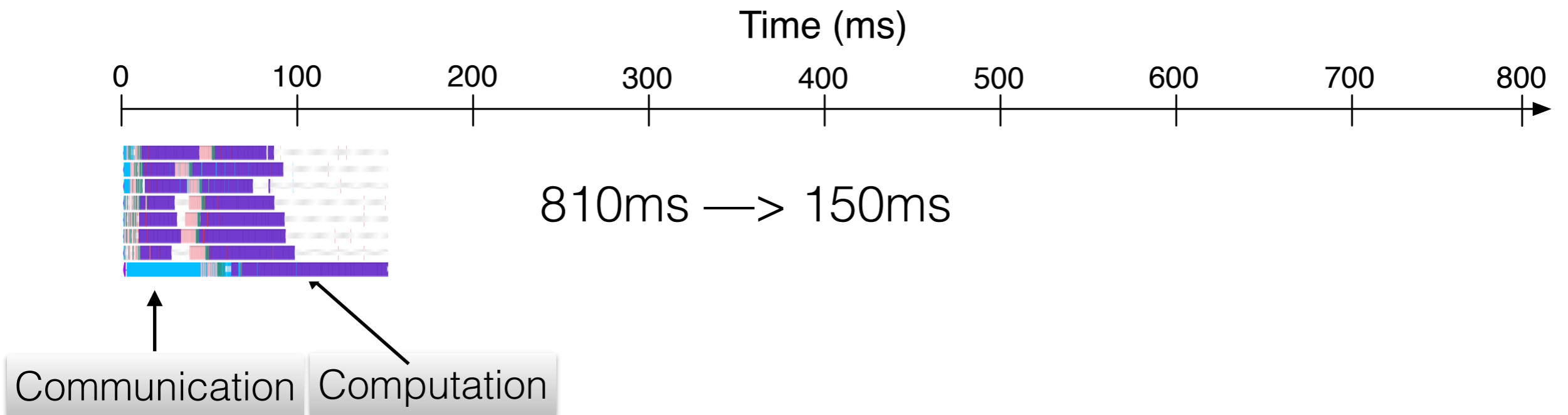
# Narrow Phase

Second Challenge: **Communication Imbalance**



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The more potential collision pairs are spread, the more communication requests.

# Narrow Phase: Communication Imbalance

Locality Aware Load Balancer

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Locality Aware Load Balancer

Potential  
Collisions

Partition 2  
&  
Partition 3



Partition 3  
&  
Partition 4



Partition 5  
&  
Partition 2



# Narrow Phase: Communication Imbalance

Locality Aware Load Balancer

Collision Tasks

Partition 2  
&  
Partition 3



Partition 3  
&  
Partition 4

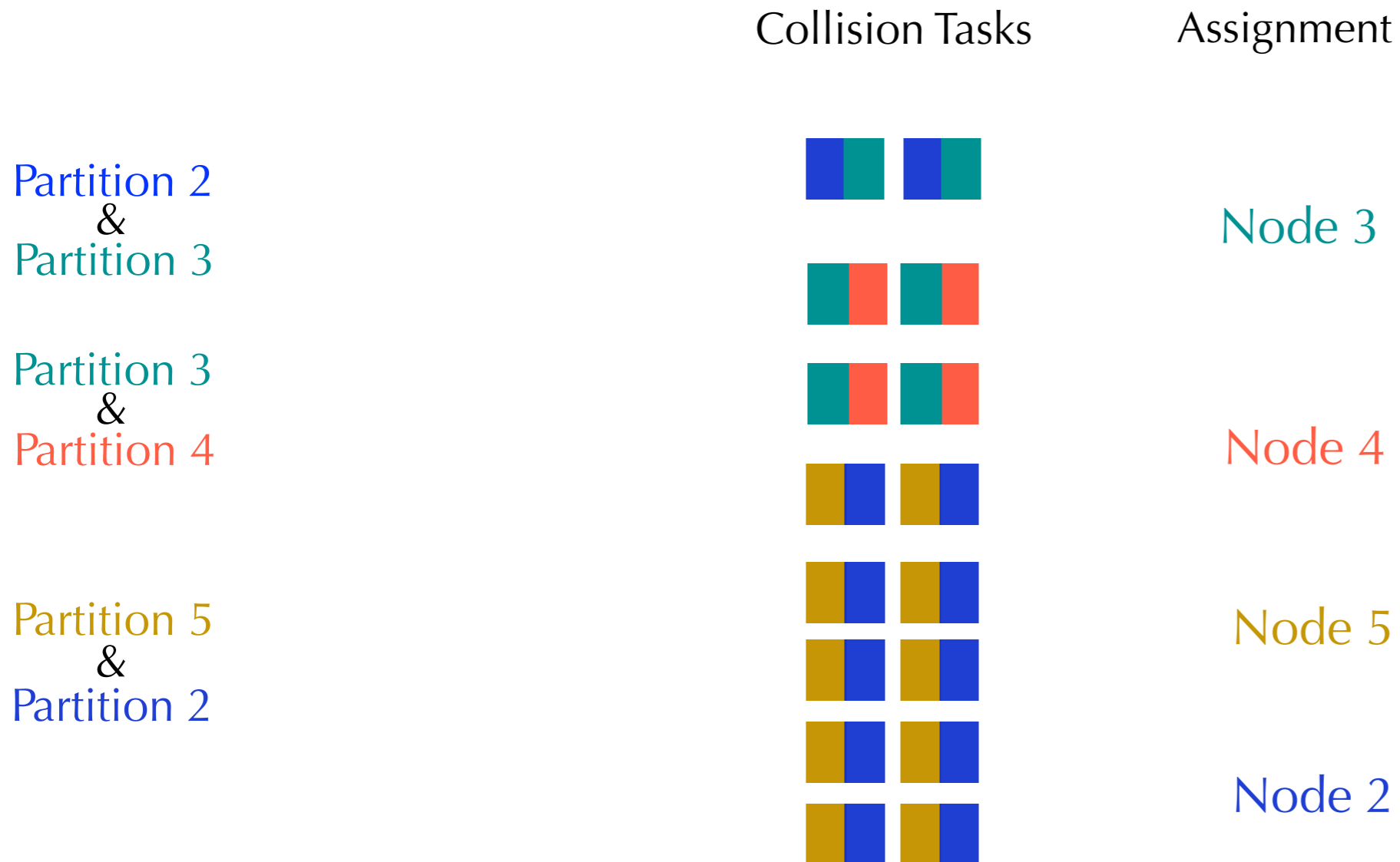


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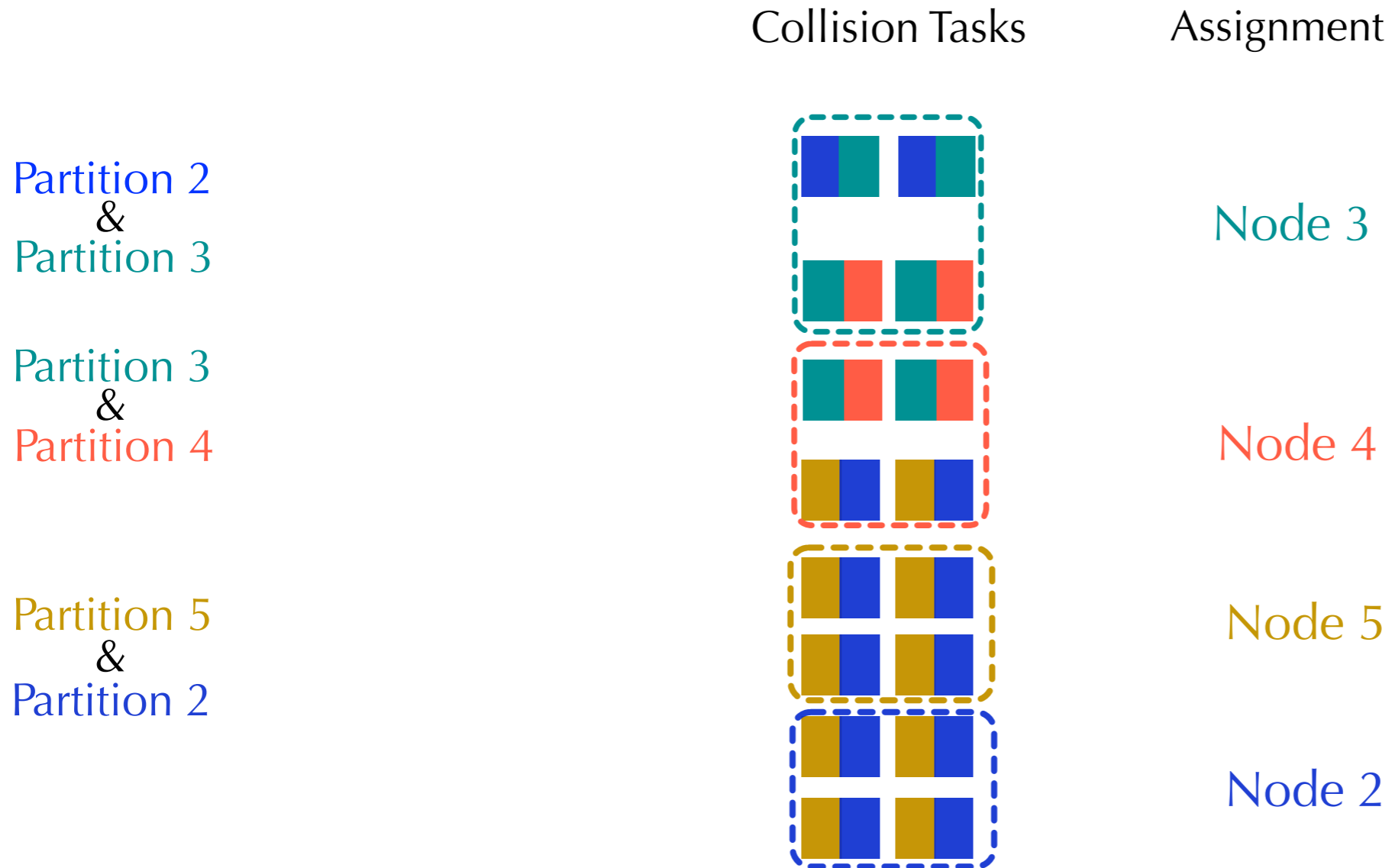
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# Narrow Phase: Communication Imbalance

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# Narrow Phase: Communication Imbalance

Overlapping Computation and Communication

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## Communication Imbalance

Overlapping Computation and Communication

**Let's look at the flow on one node**

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**L**  list of potential collision tasks

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Overlapping Computation and Communication

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**M.type()**

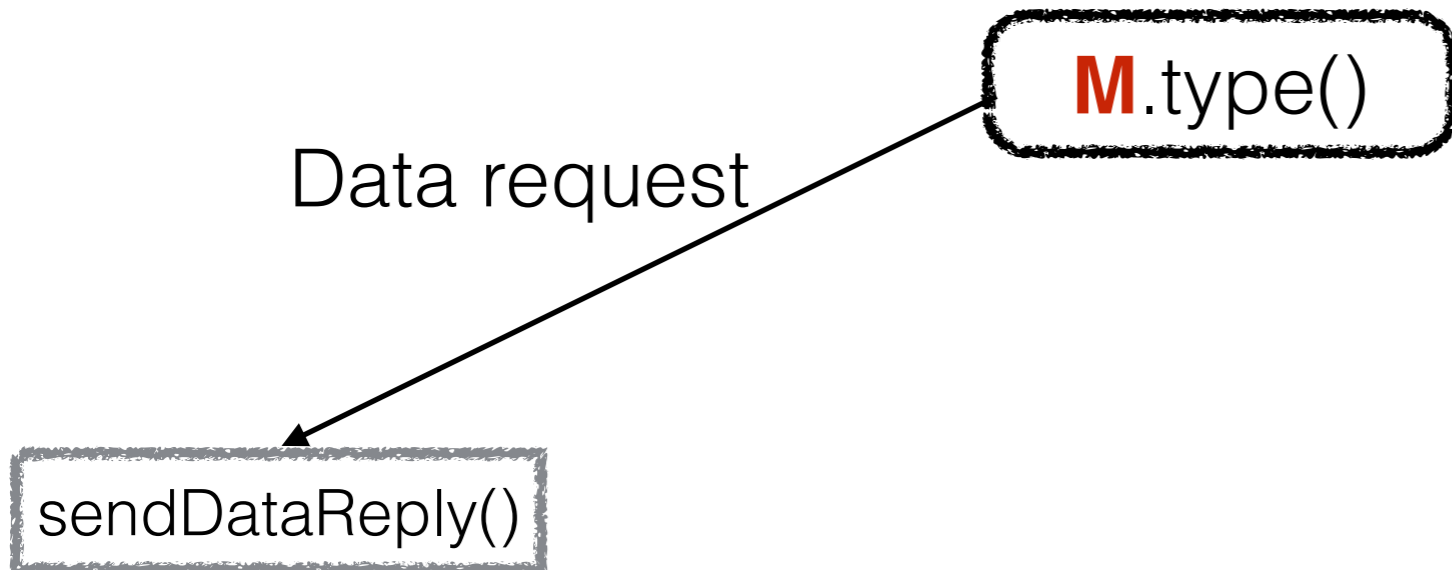
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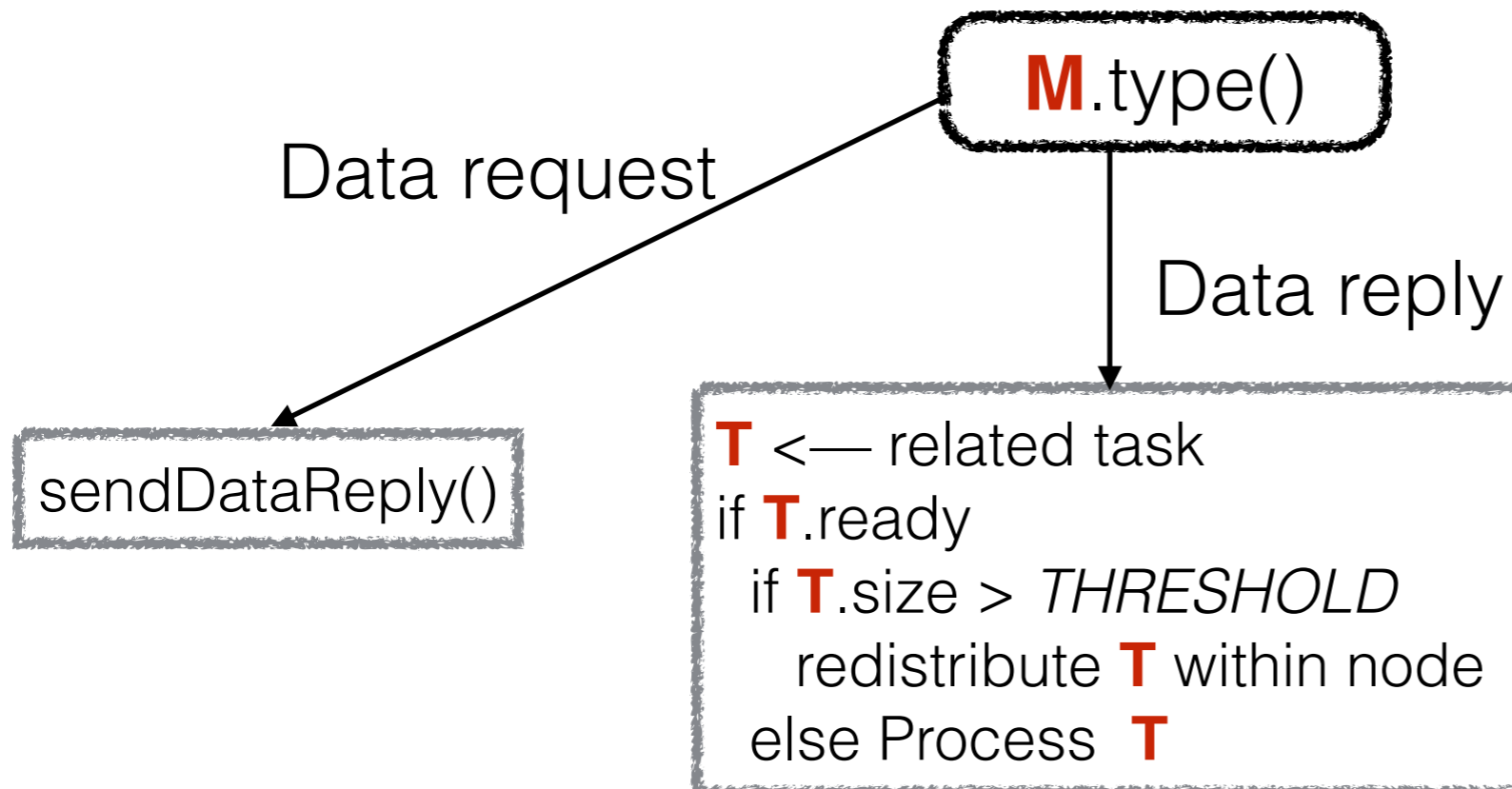
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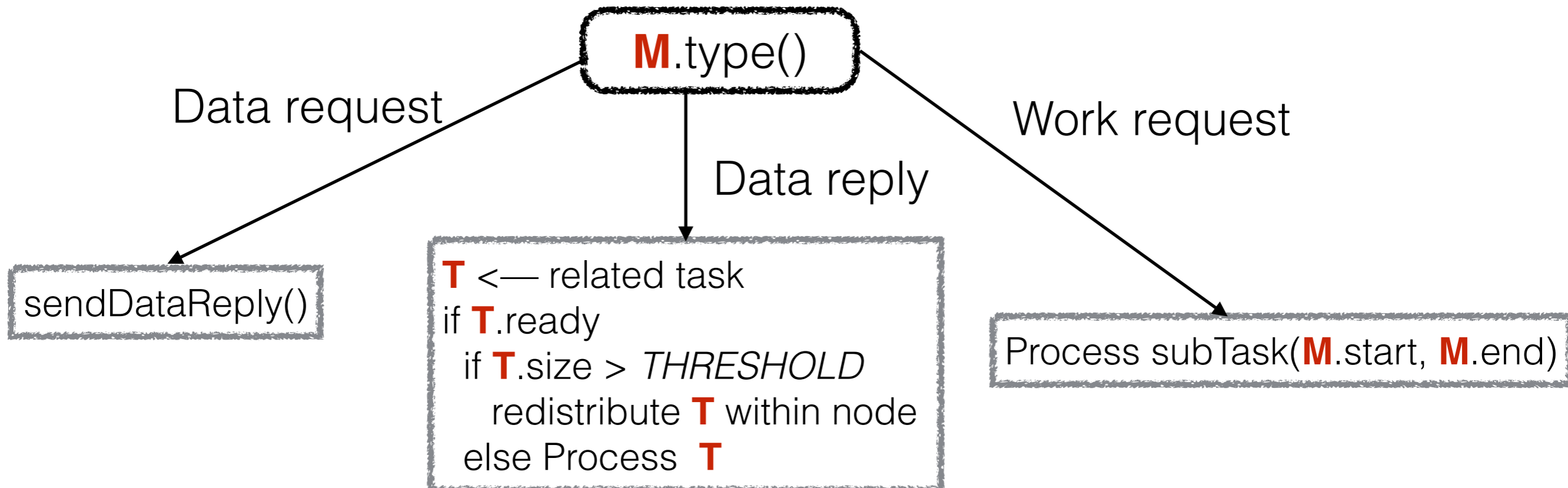
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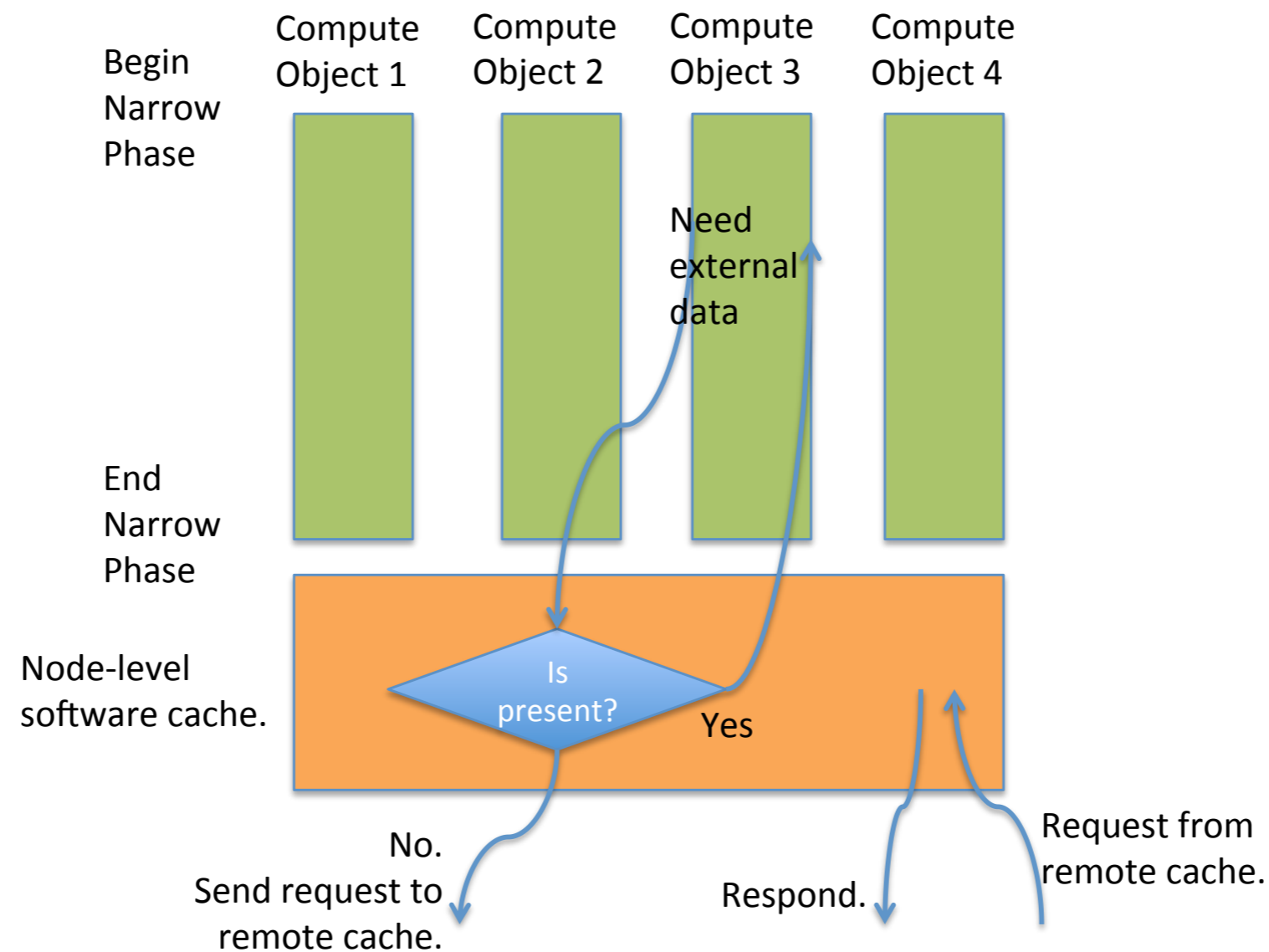
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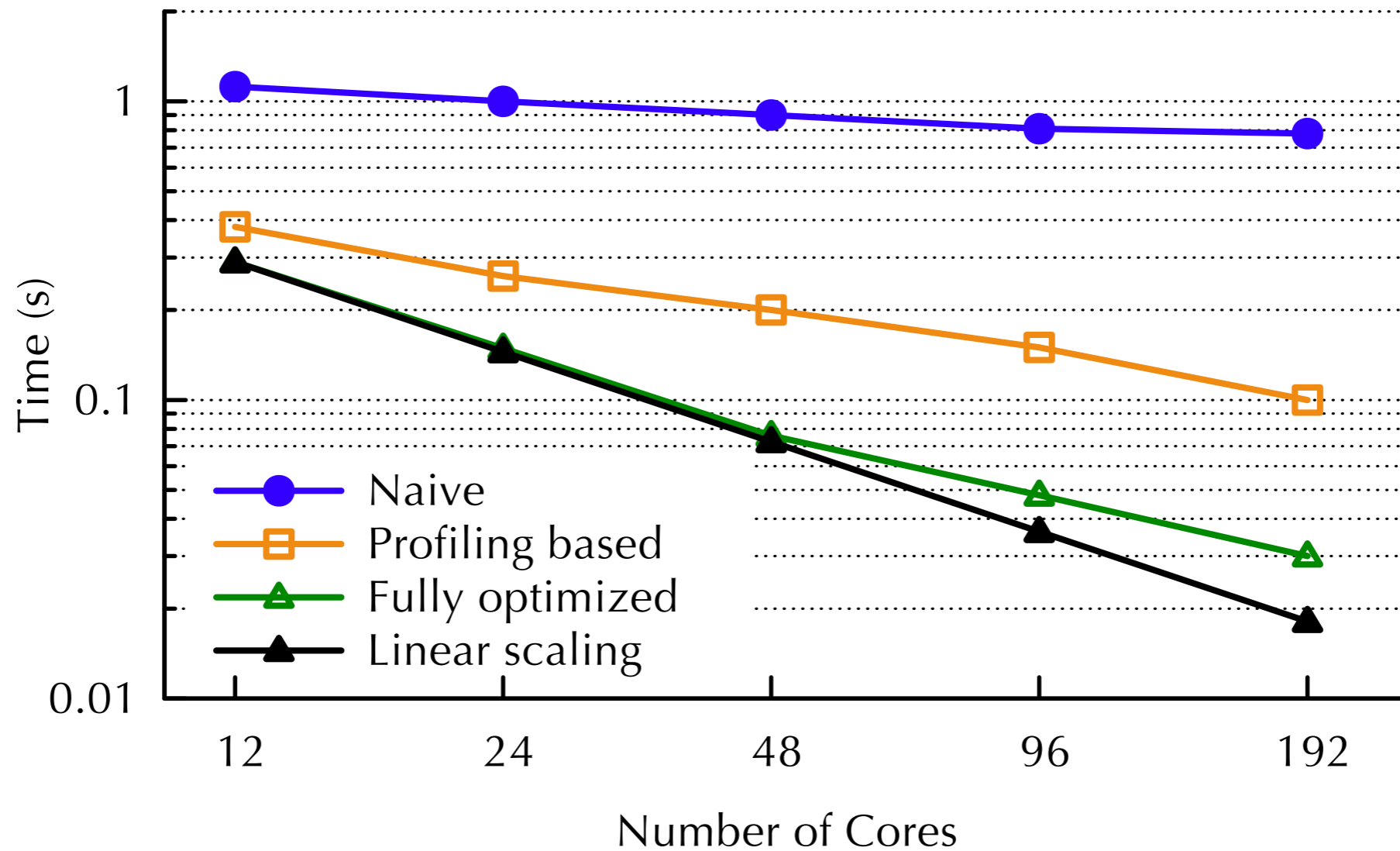


# Narrow Phase: Communication Imbalance

Node level data cache

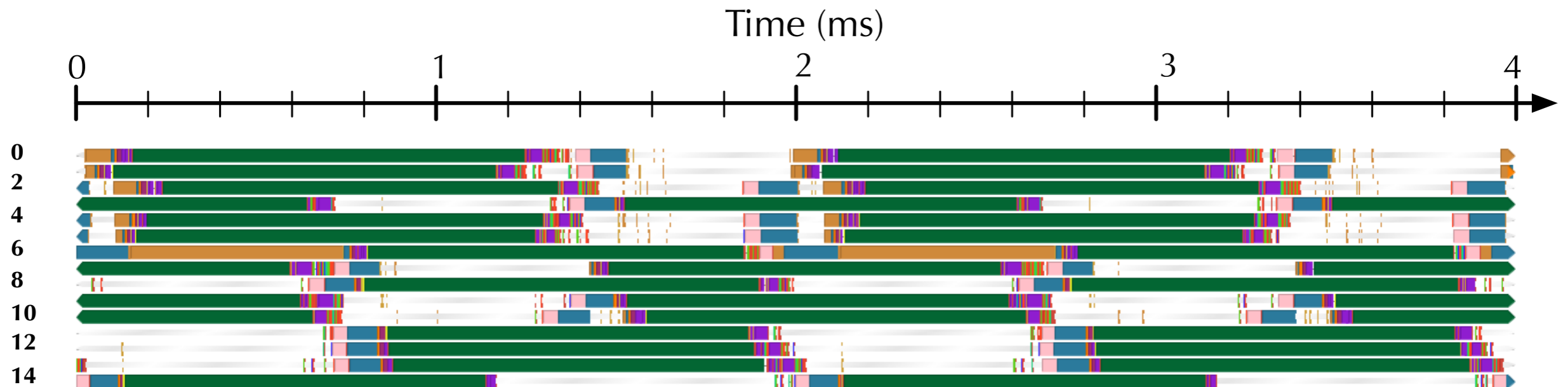


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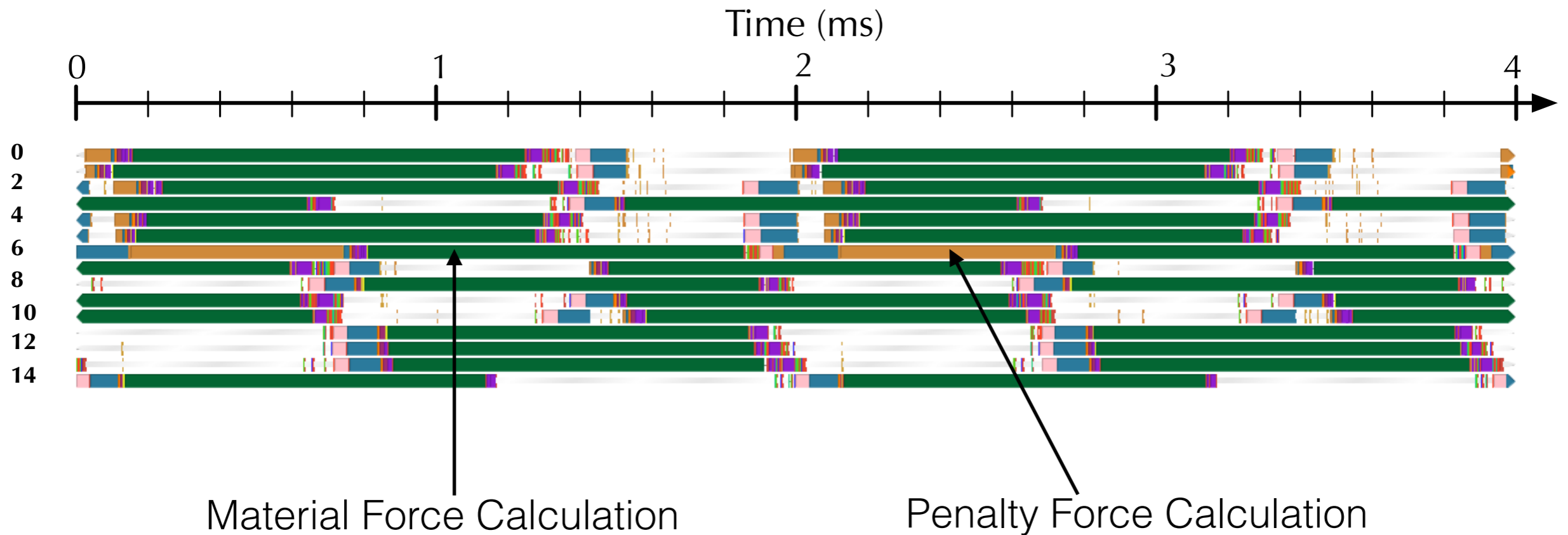
# Collision Response

Computation Imbalance



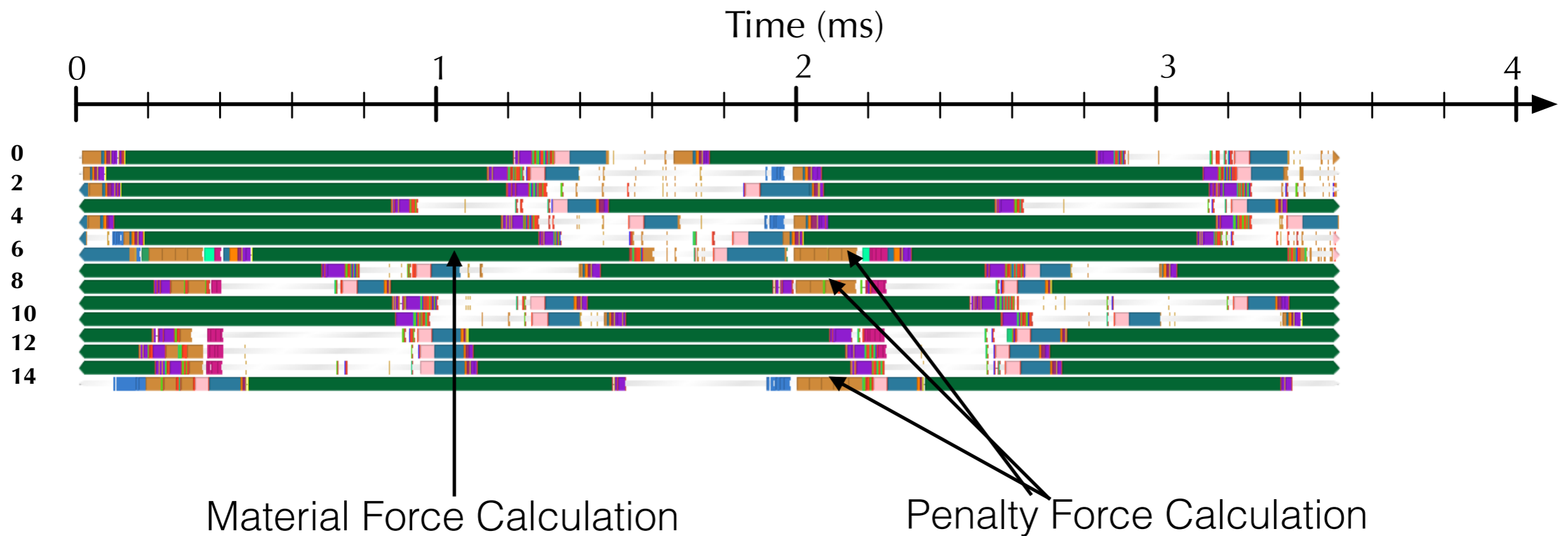
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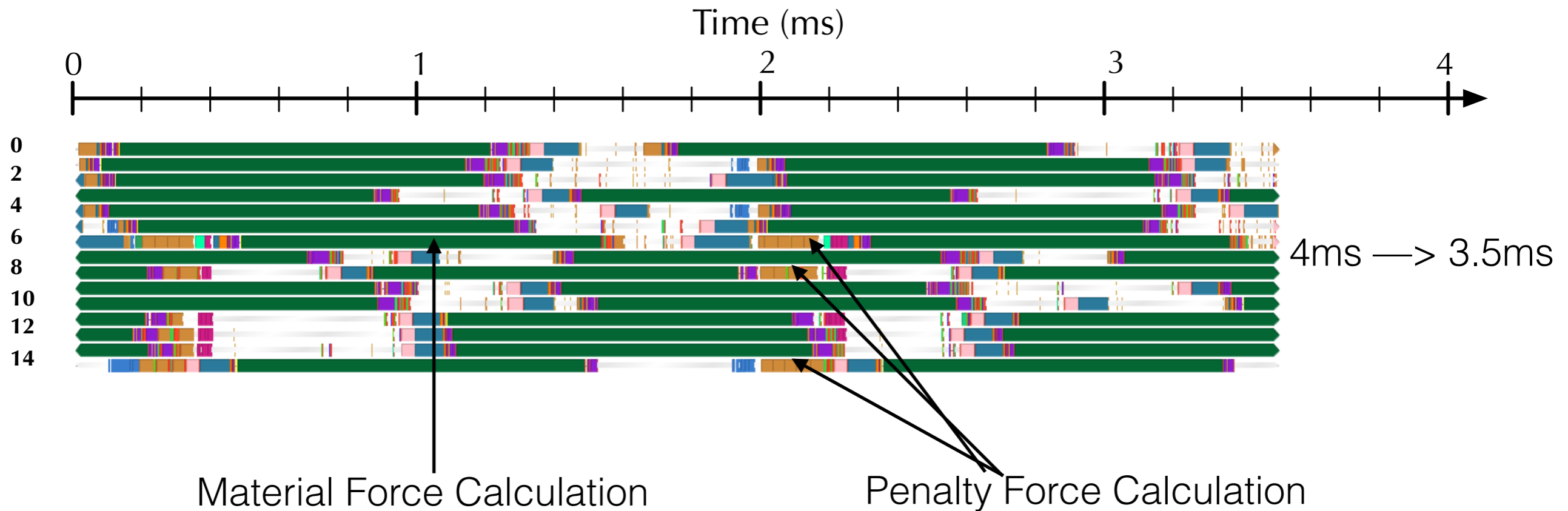
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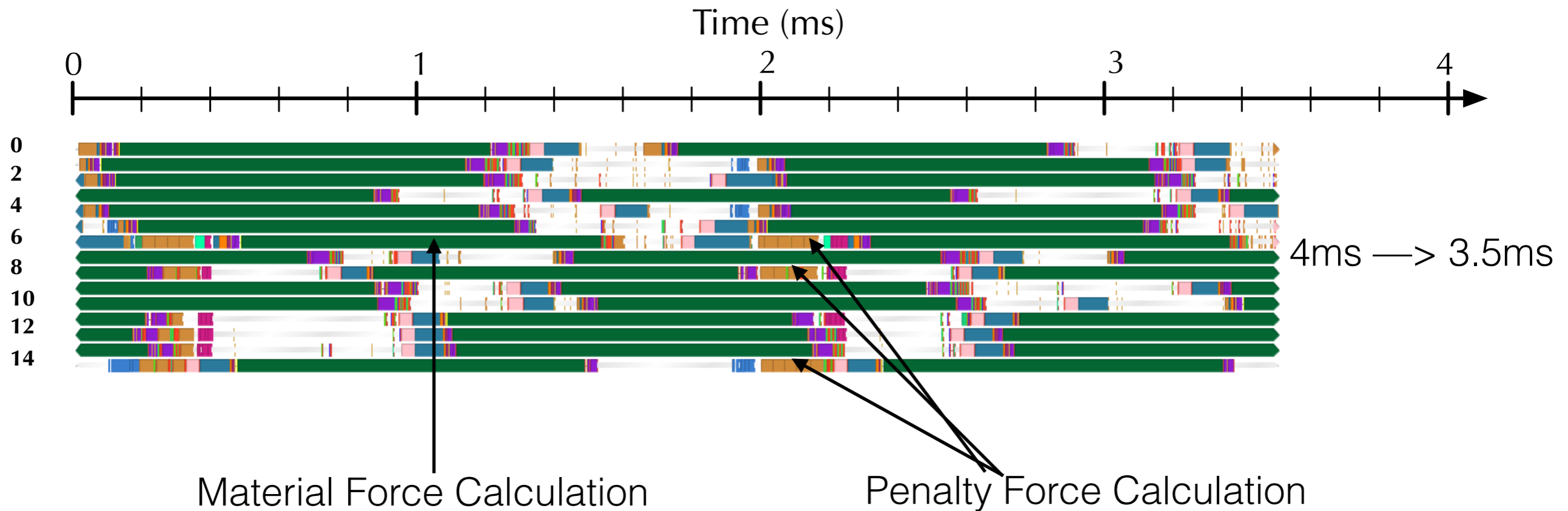
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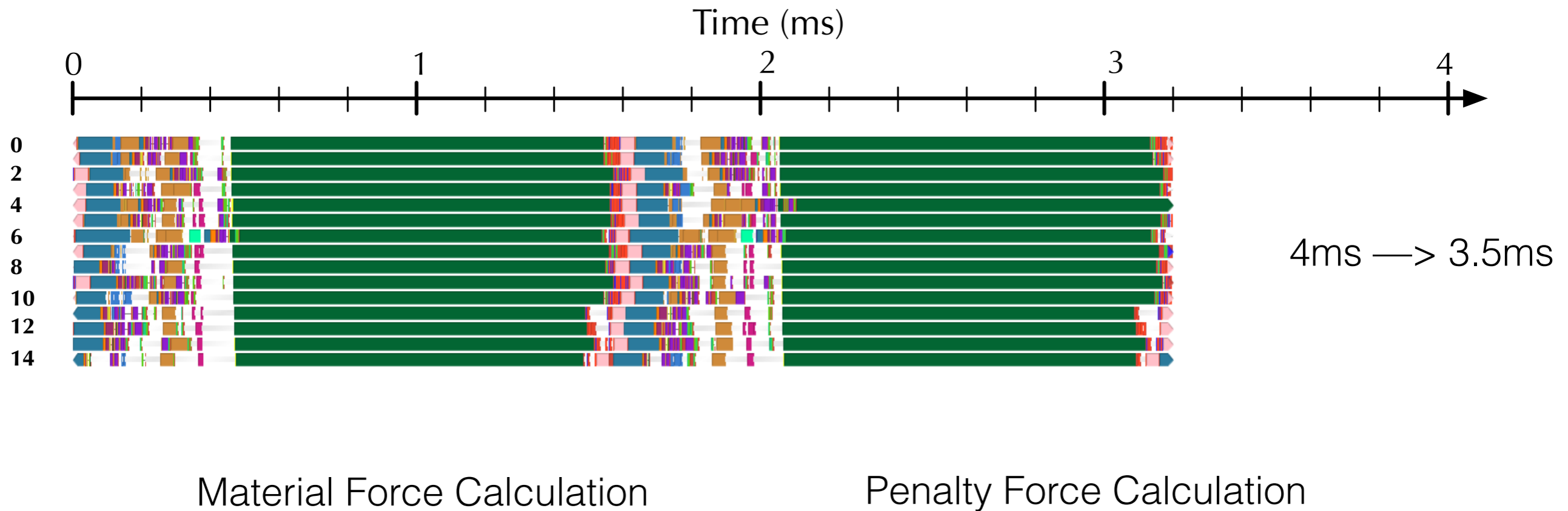
# Collision Response

Importance of partial barrier



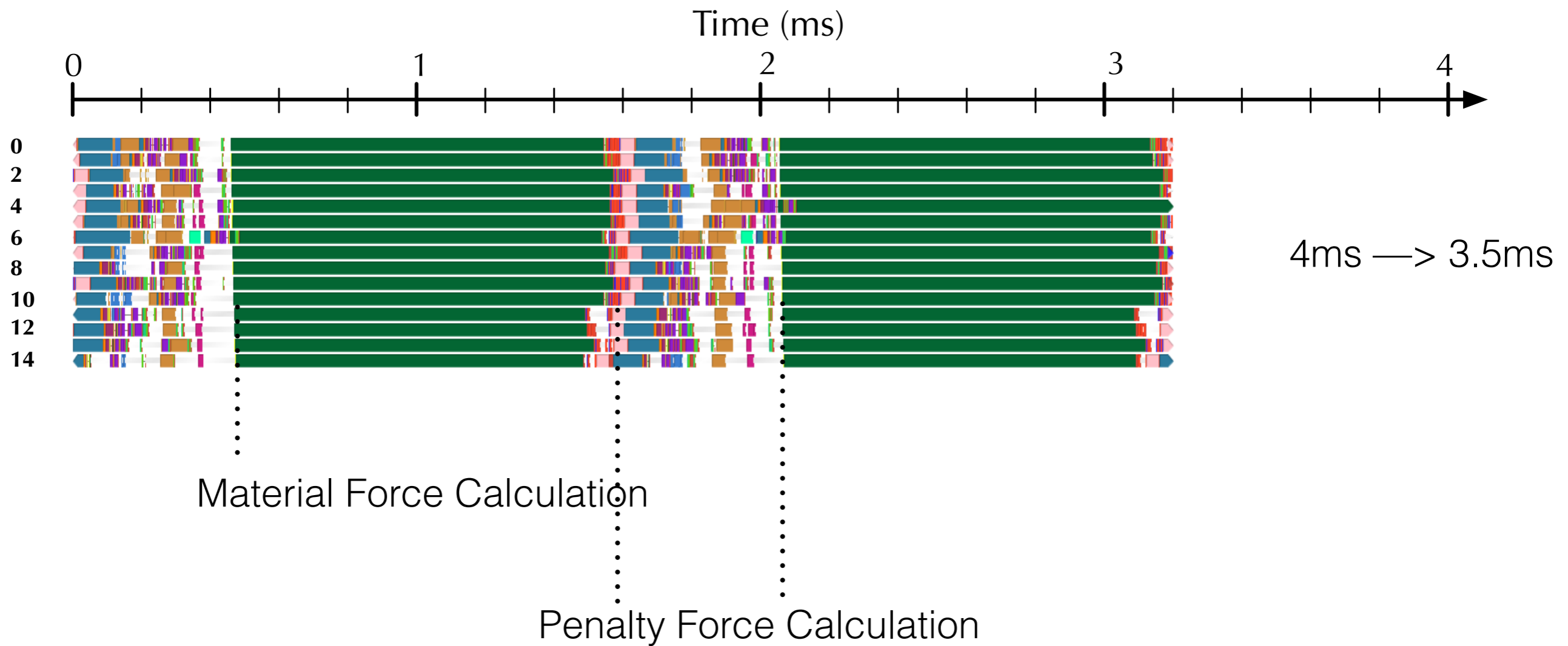
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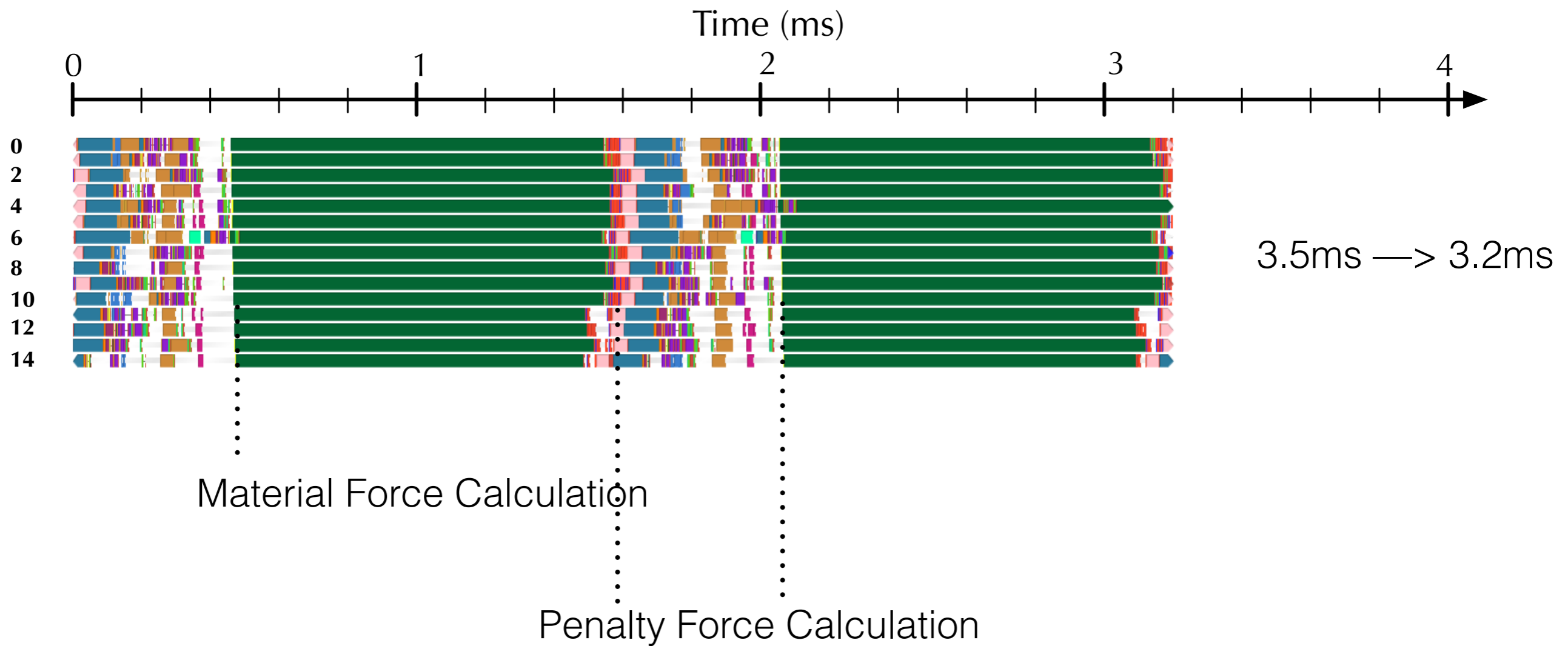
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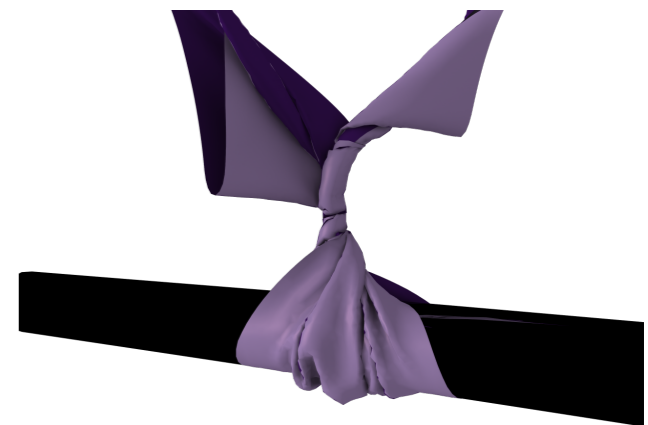
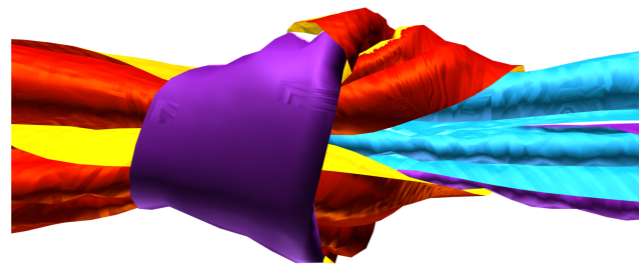
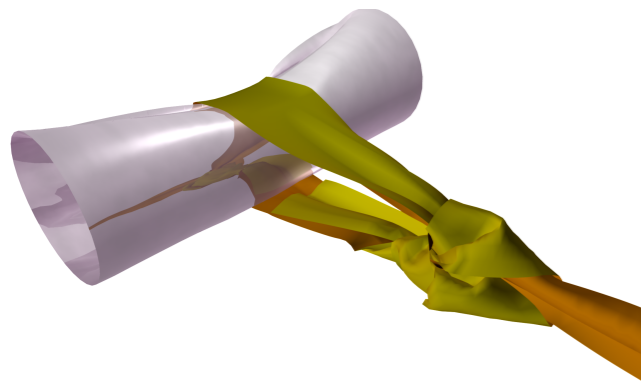
# Results

## Machines

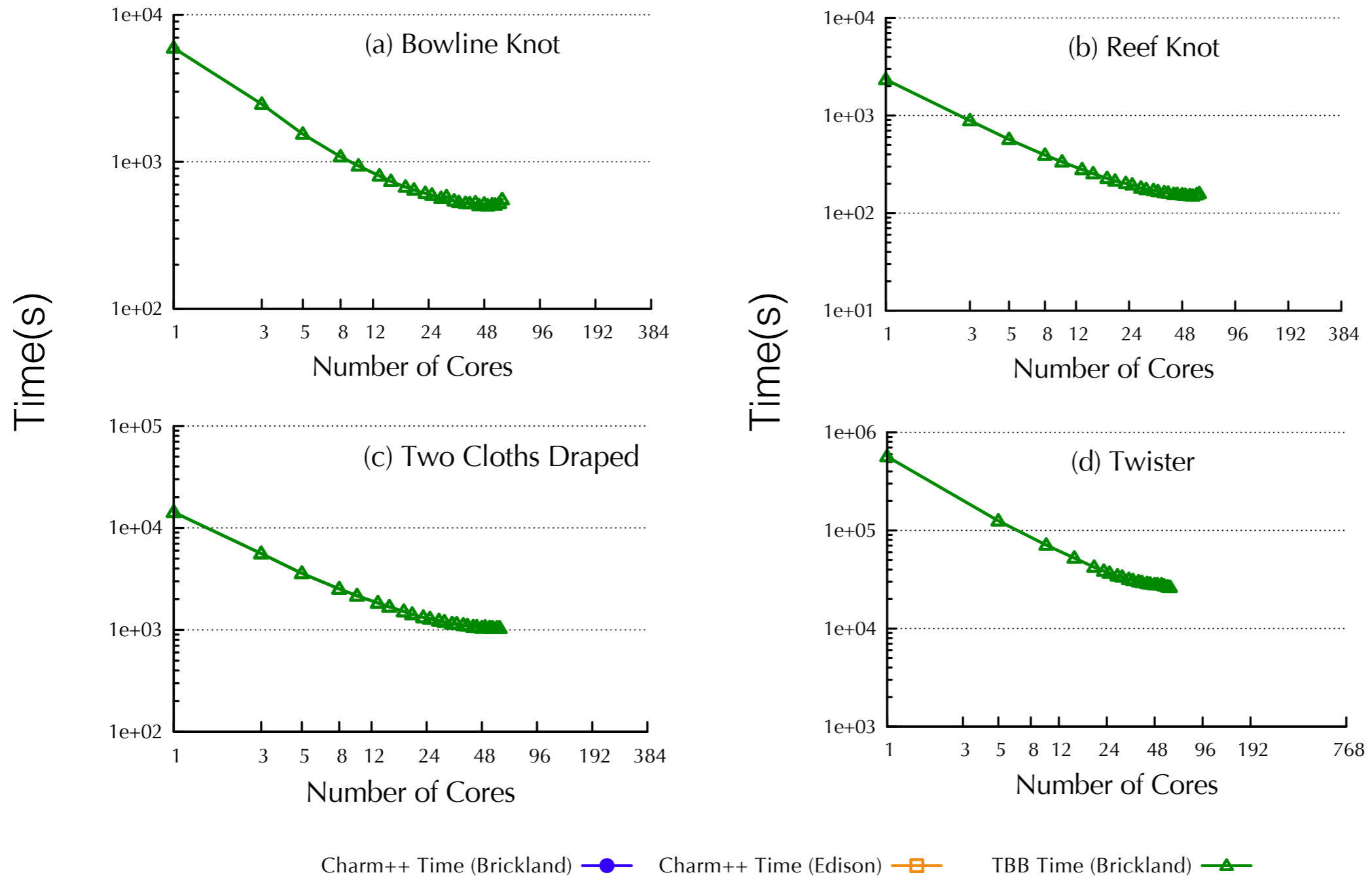
**Edison:** a Cray XC30, Intel E5-2695@2.4GHz, 12 core Ivy Bridge

**Brickland:** a 4 socket system with Intel E7-4890@2.8GHz, 15 core Ivy Bridge

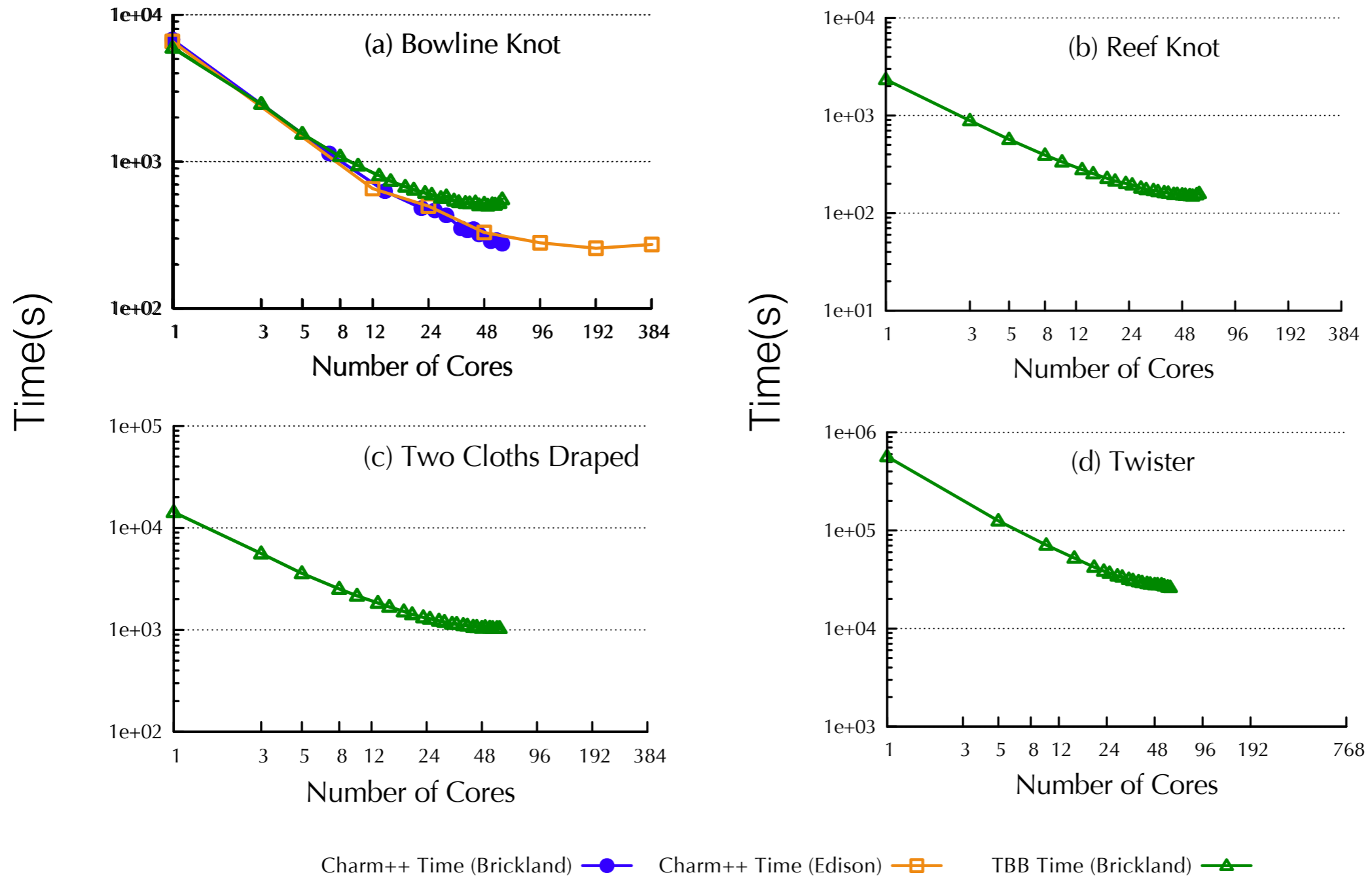
## Examples



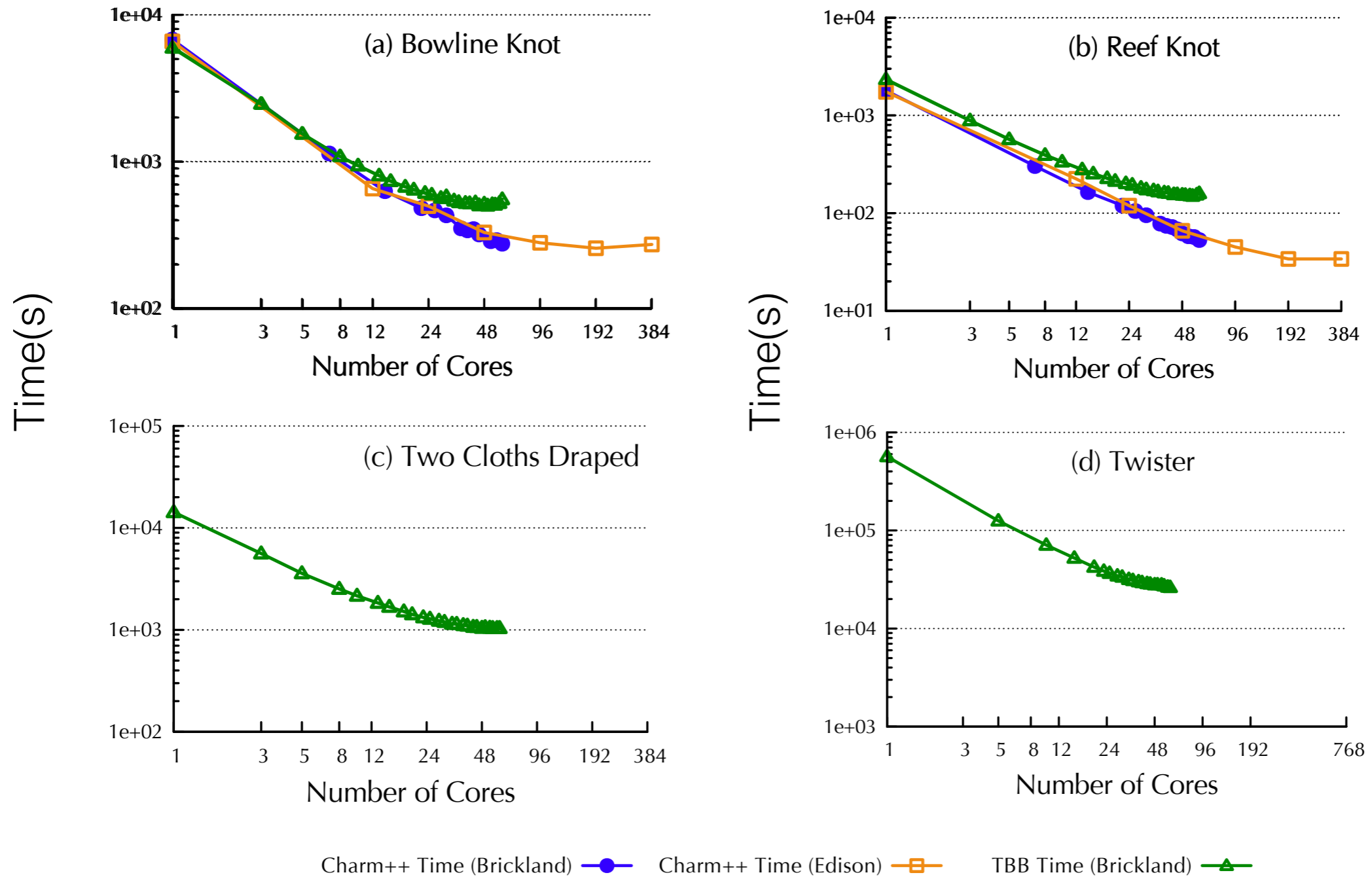
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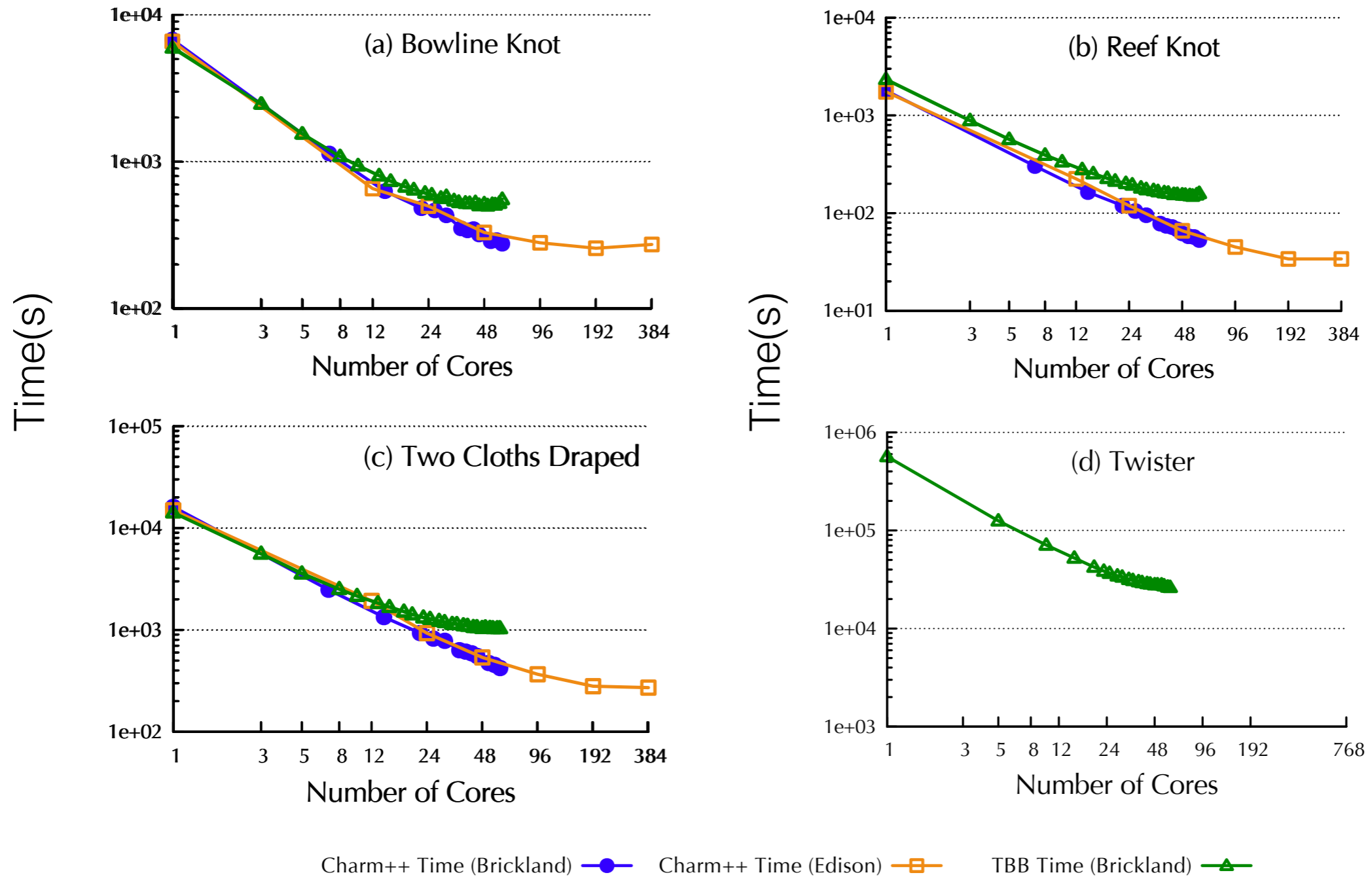


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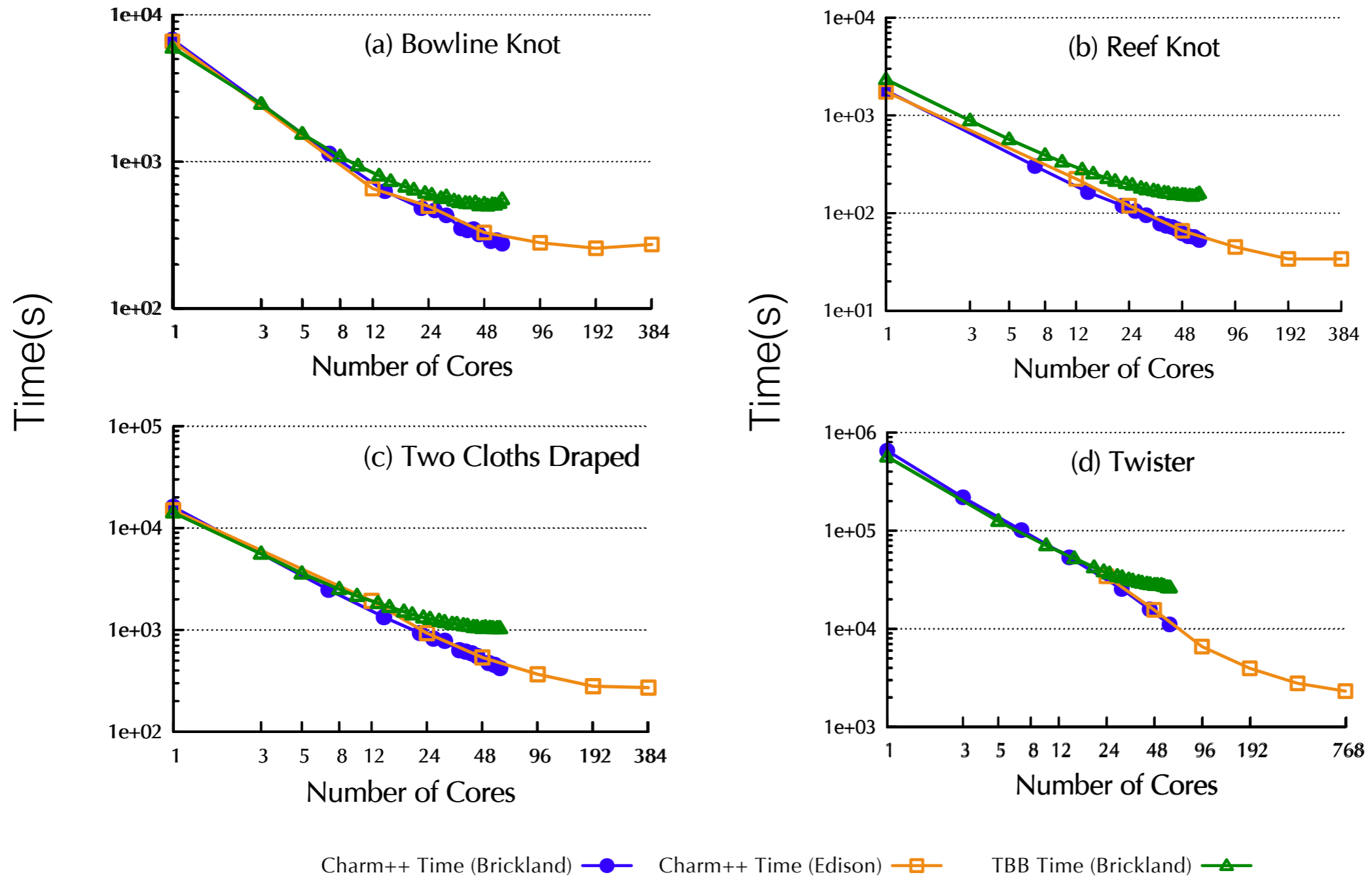




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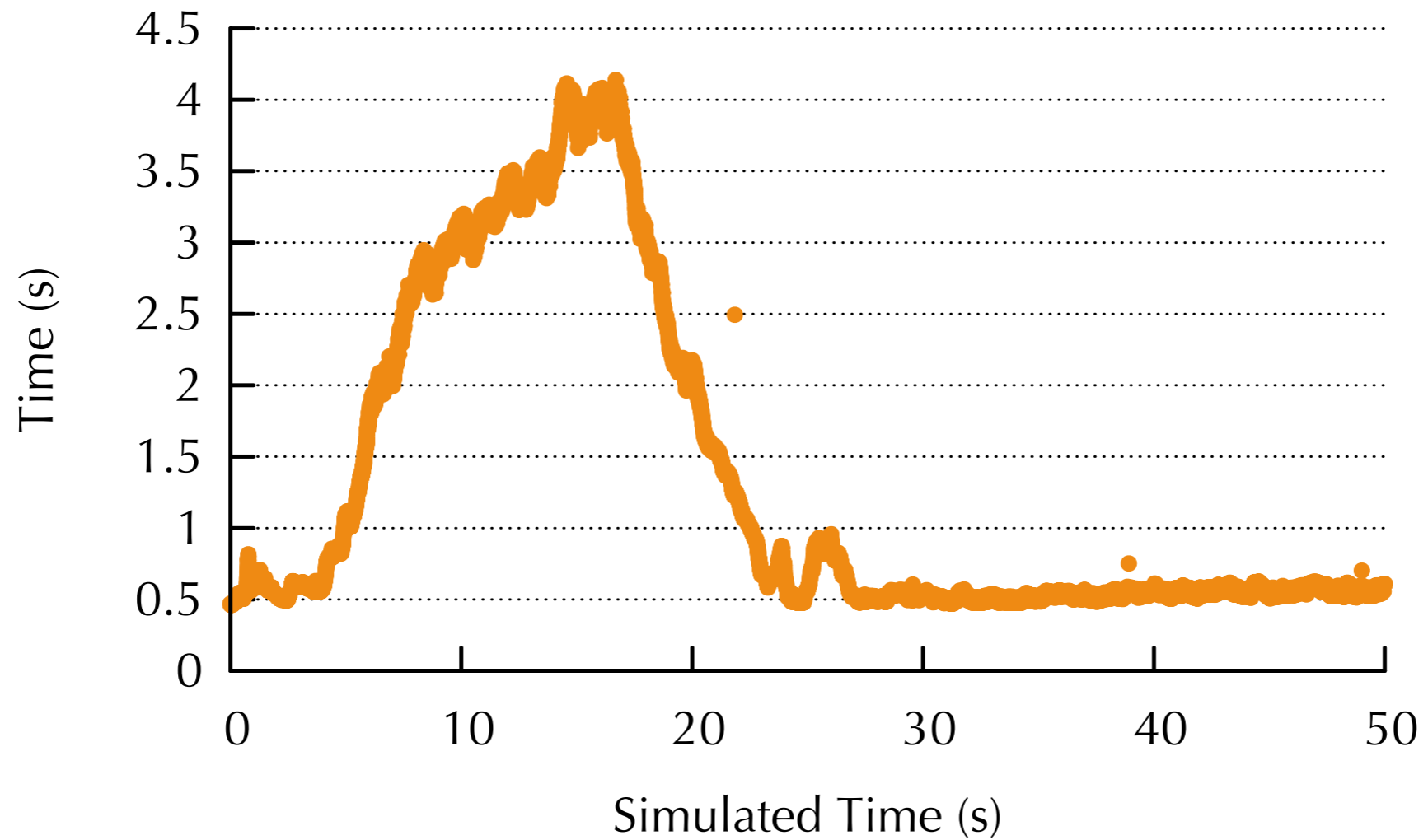
# Long Twister



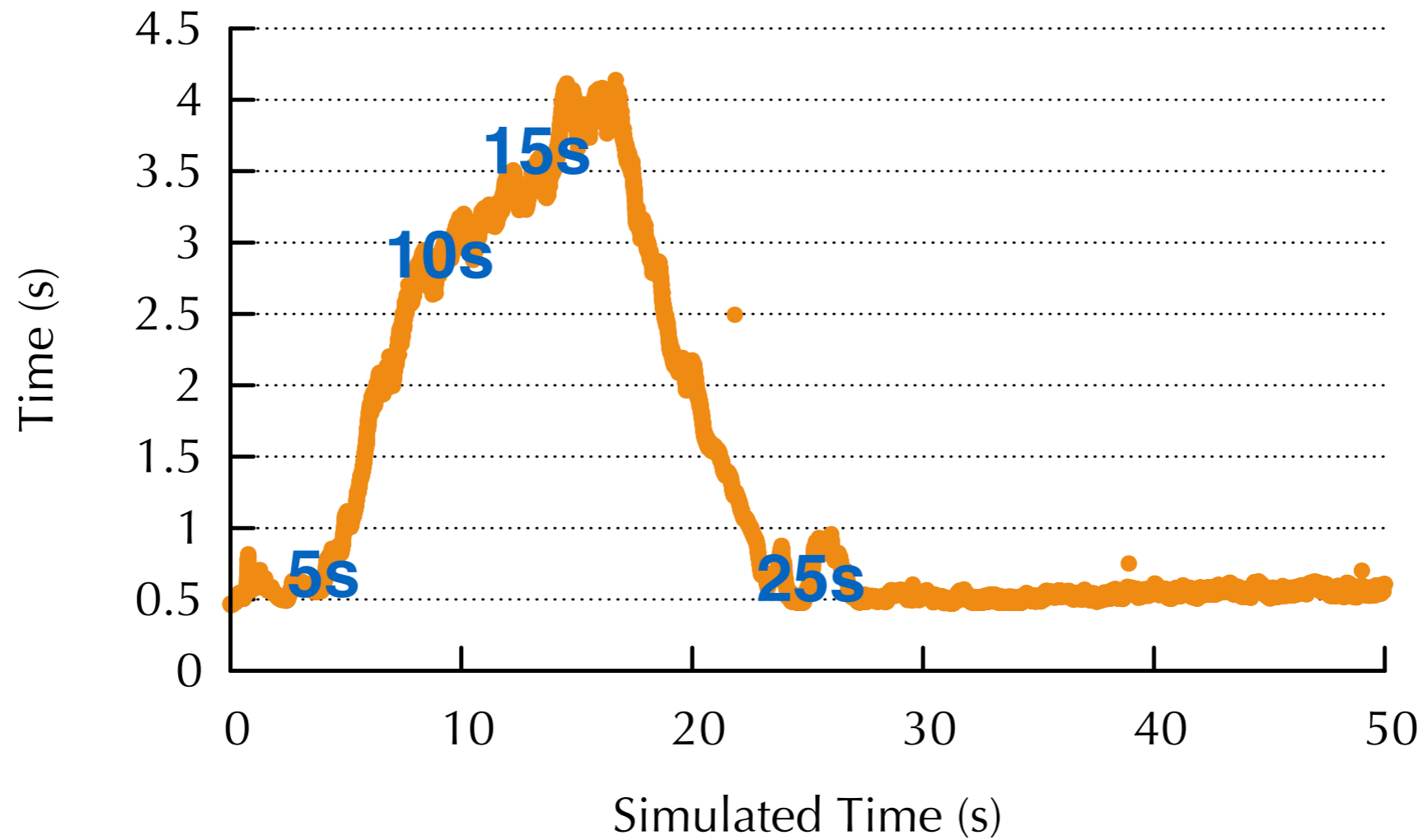
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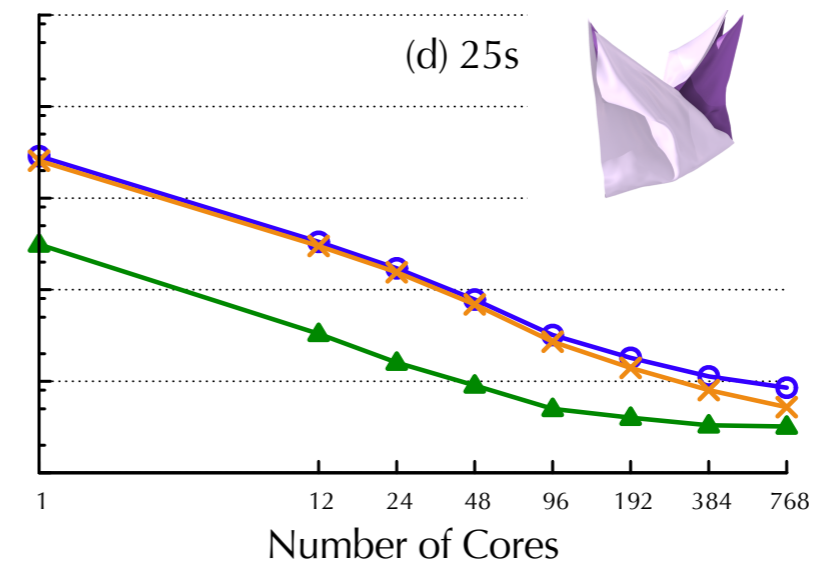
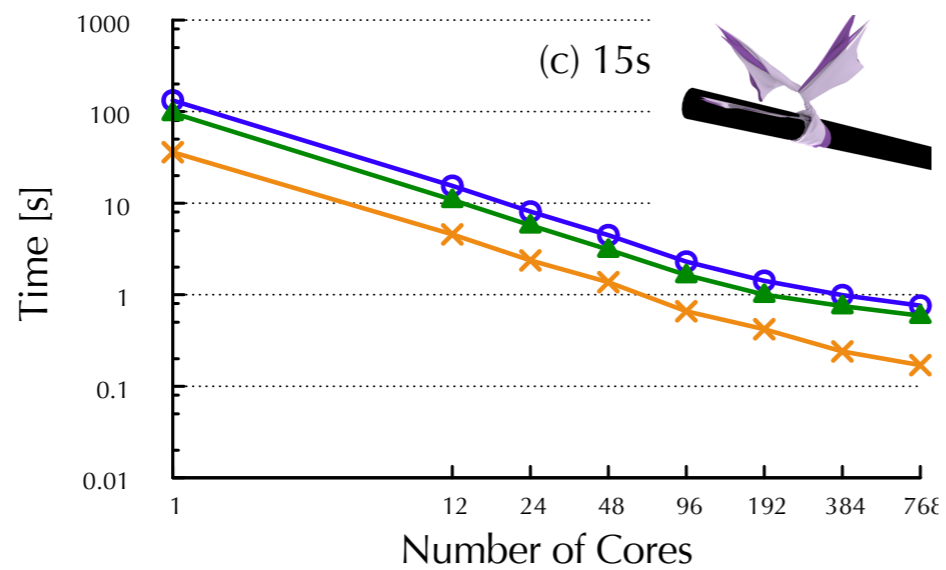
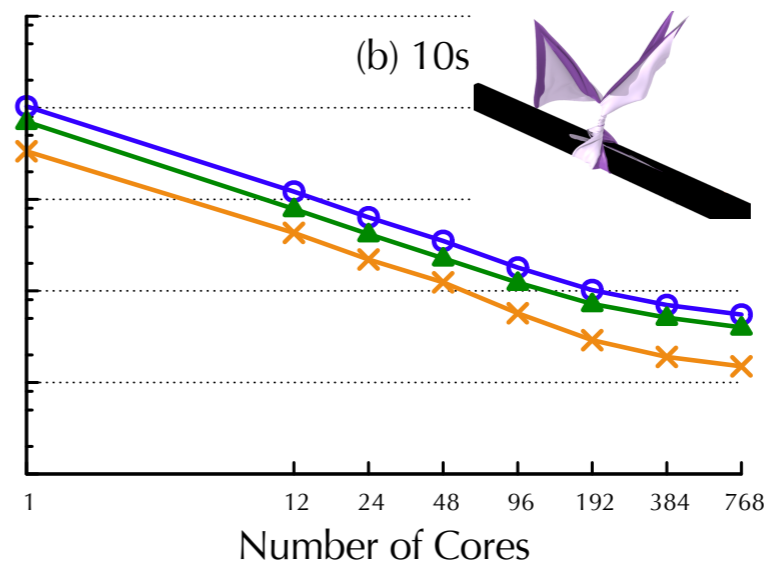
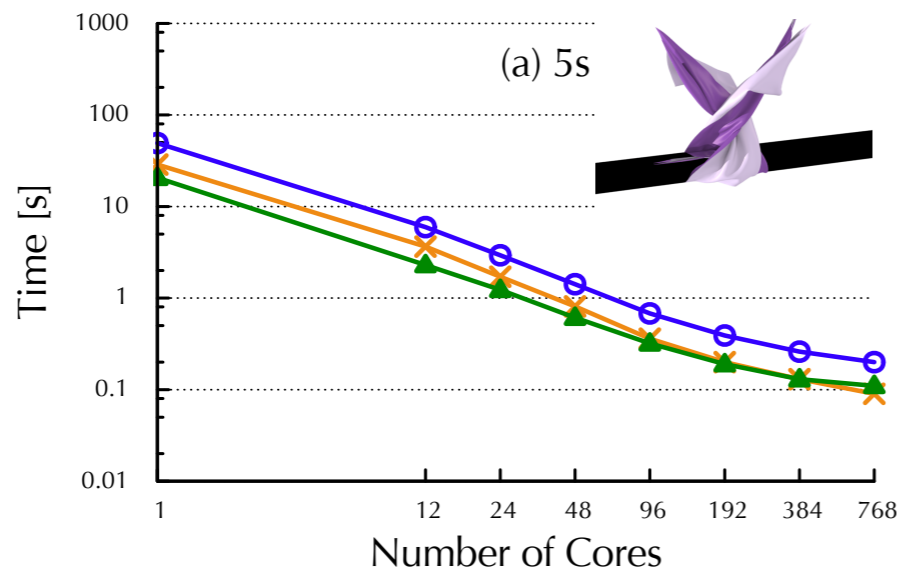
# Long Twister



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# Long Twister



Time per window —○— Force calculation —×— Collision detection —▲—

# Conclusion

- Strong scaling to 384 cores on Edison
- More than 10x speedup compared to the TBB shared memory results
- Charm++ is well-suited for dynamic irregular applications like ACM
- Message-driven feature helps the overlapping of communication and computation